

Developing rad-hard Chip-on-Board Fiber-Optics

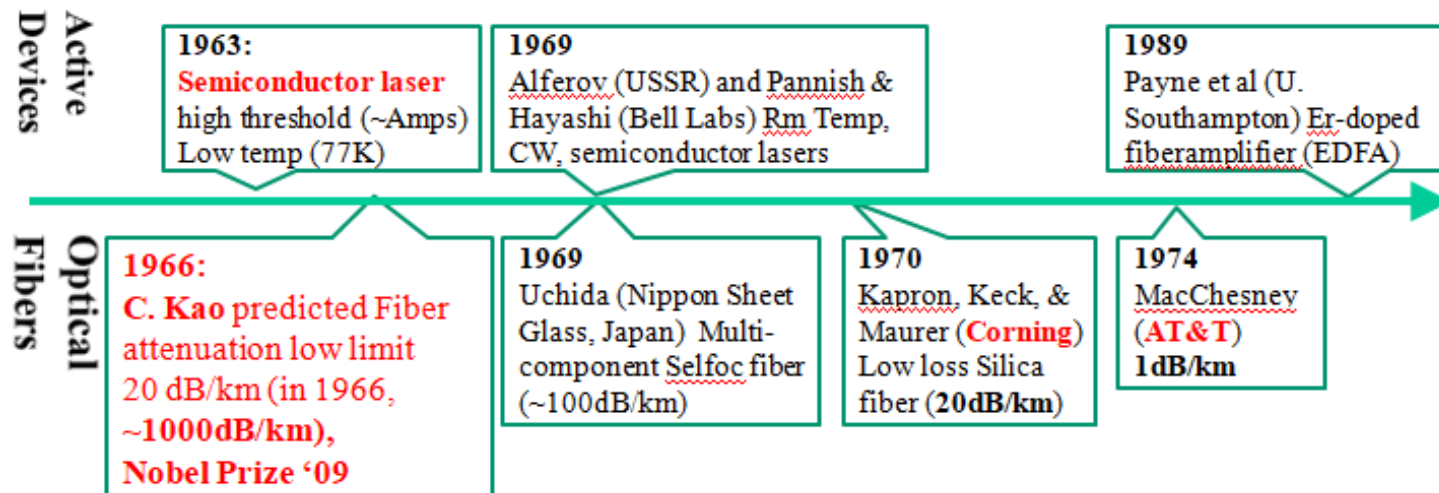
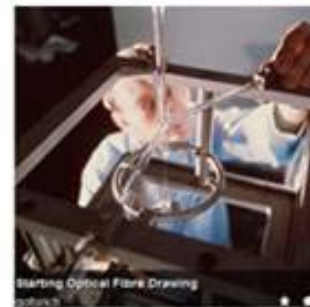


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2024.06.19

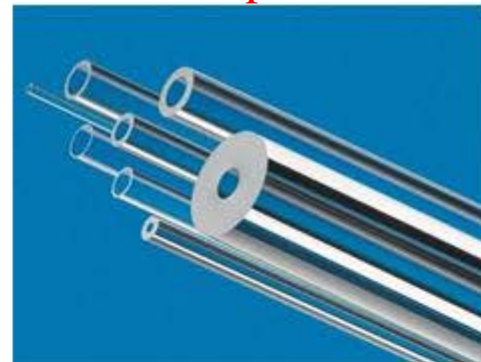
- **Fiber rad-hard**
MM Ge-doped Co60 TID
- **Opto CMOS rad-hard**
850 nm VCSEL, PD
NIEL, TID
- **HEP optical links**
experience, future
Chip-on-Board assembly



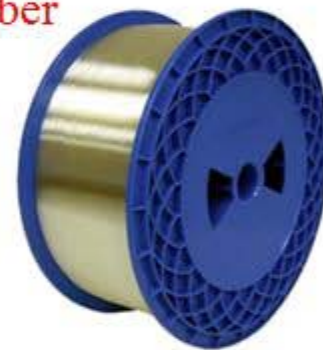
Optical fiber making

PCVD plasma chemical vapor Deposition
MCVD modified chemical vapor deposition
OVD outside vapor deposition
VAD vapor axial deposition

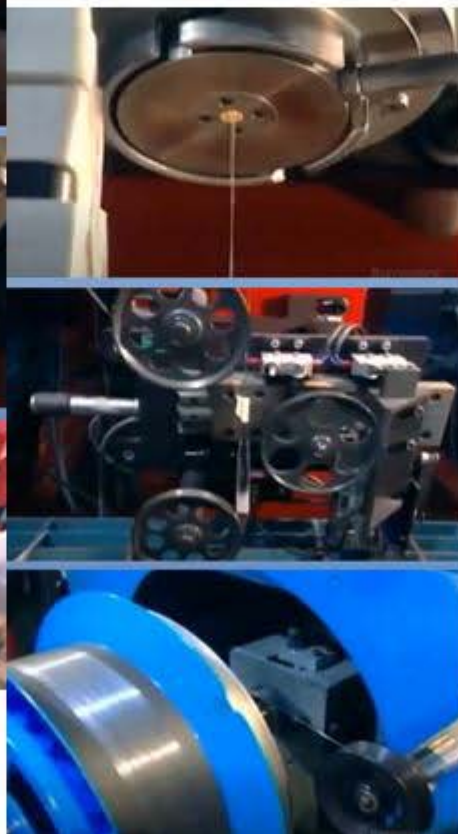
Fused Silica Tubes,
pure SiO_2
for Fiber Optics,



Bare Fiber
Reel



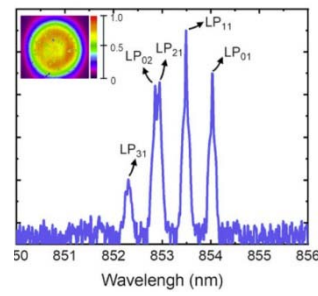
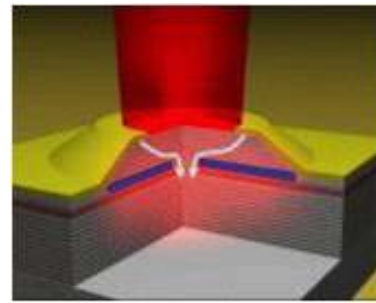
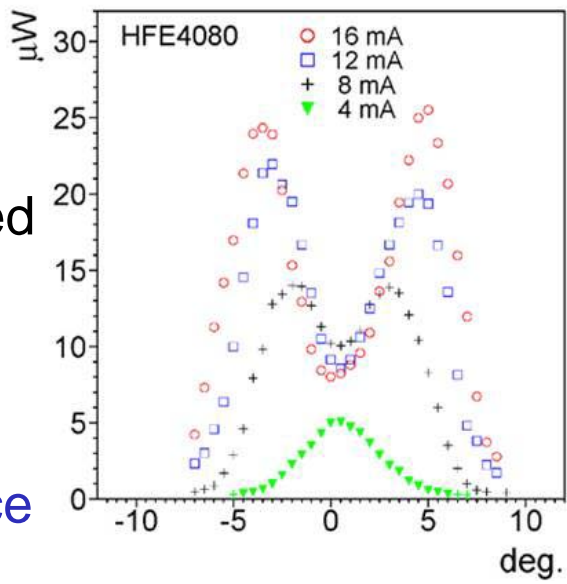
Fiber Cable



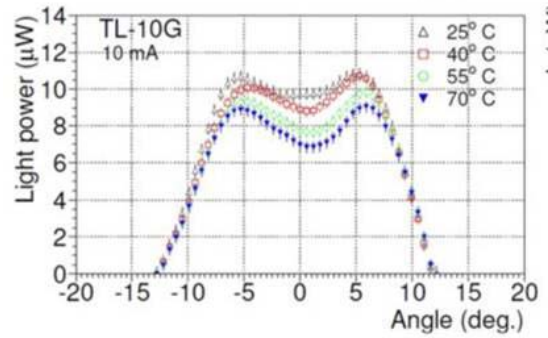
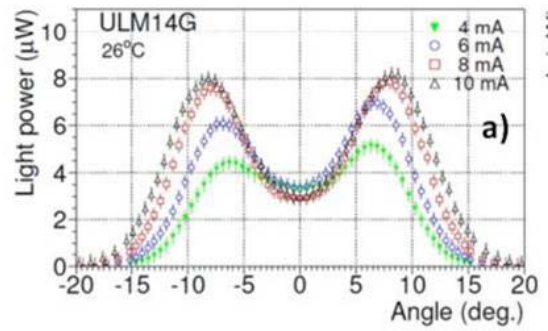
Looking for testing 武汉 YOFC
长飞光纤 Ge-doped, Rad-hard fibers

Light Coupling efficiency

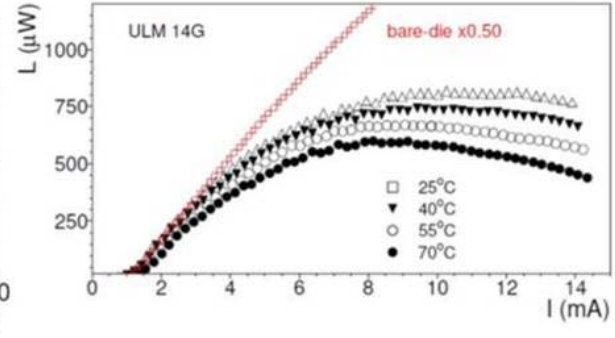
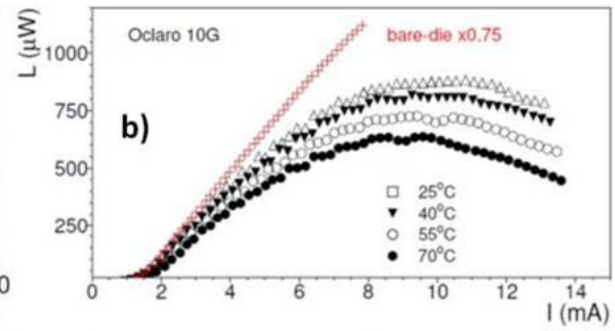
- **VCSEL far-field distribution**
at low current, 0th mode is centralized
higher modes pop-up in outer-rings
wider angles
- **Coupling to Lens**
loss to reflection, angular acceptance



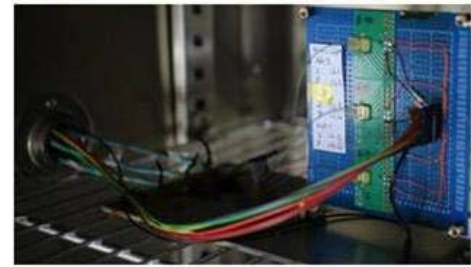
Far-field angle vs I, T



L-I, FOCI lens coupled



VCSEL+Lens in Oven



Ge-doped MM fiber, Co-60 test

Fiber Radiation Induced Attenuation (RIA)

$$\text{RIA} = (\text{IL}(0) - \text{IL}(t)) / \text{Length}$$

$$\text{IL}(\text{dB}) = 10 \times \log_{10} (P_T / P_R)$$

IL insertion loss

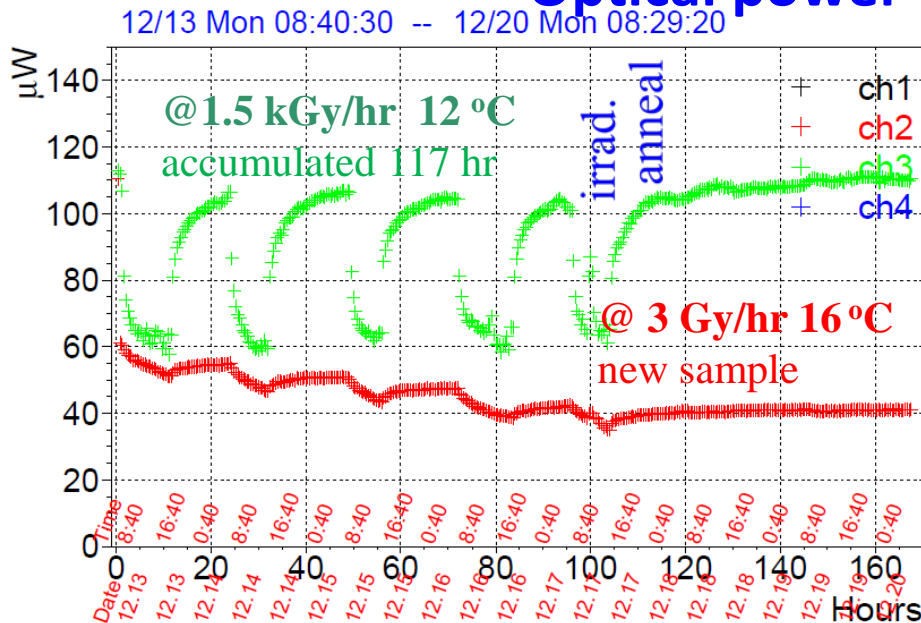
P_T transmitter, P_R received

Dependences : **Dose rate, Temperature**

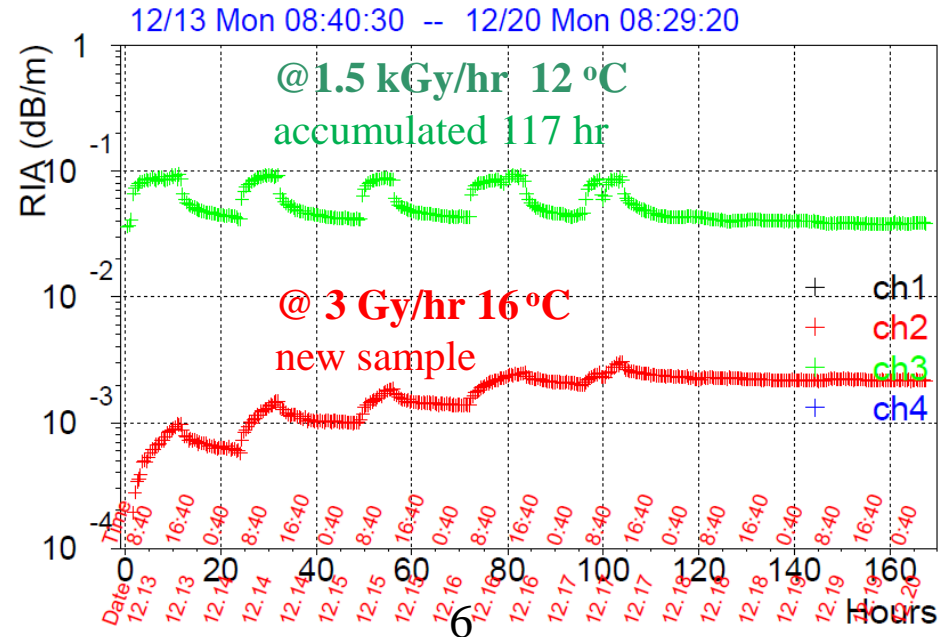
Fibers tested at Dose rate 1.5kGy/hr

Irrad ~8hr daytime, anneal overnight, over 1 week

Optical power



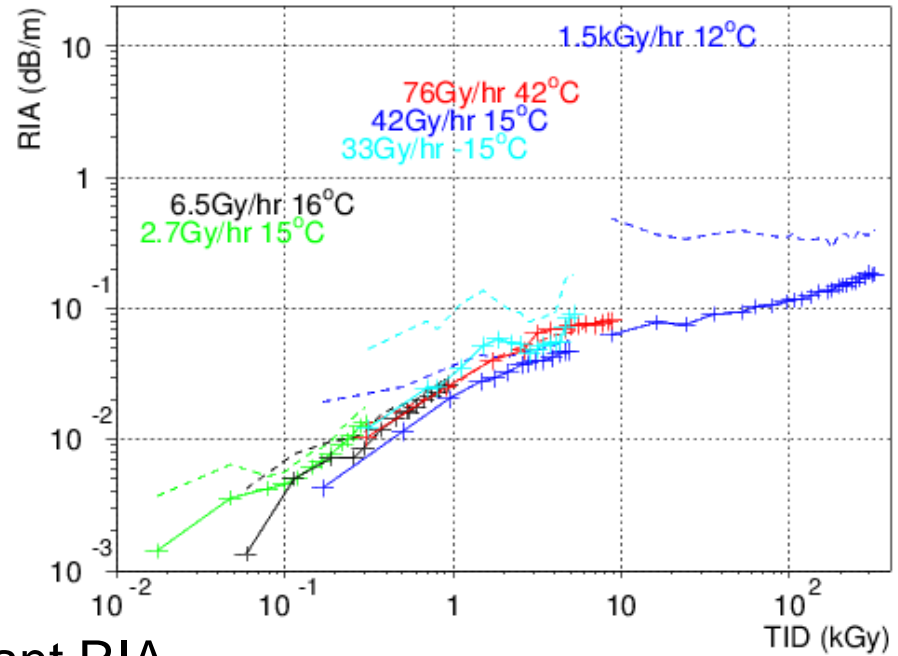
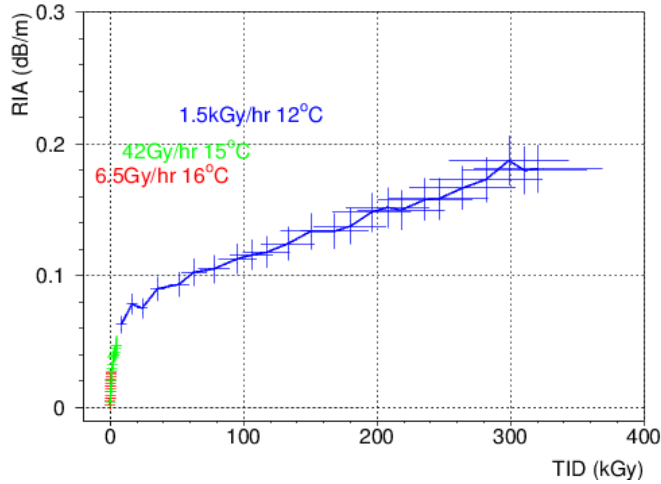
RIA



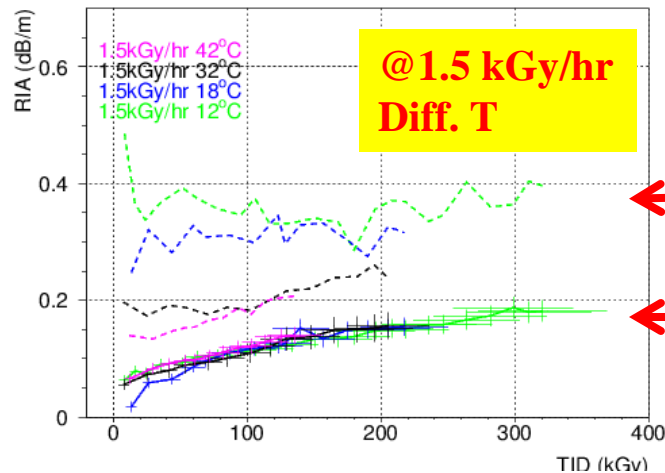
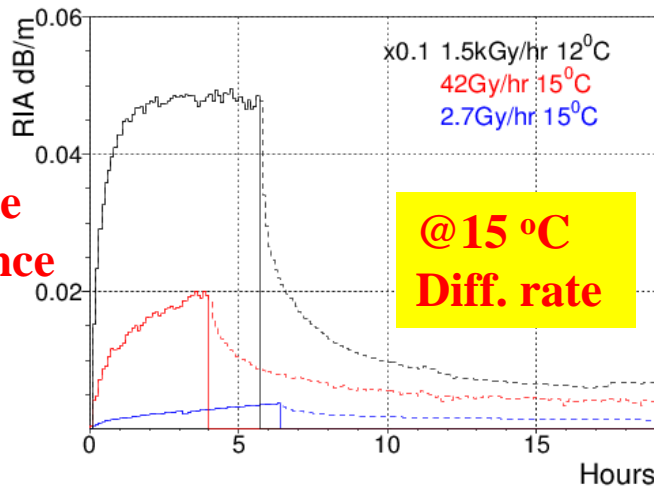
RIA, Ge-doped MM fiber (POFC fibers)

Annealed RIA

Plot Instant RIA, at daily max dose,
Plot RIA after 10 hr annealing



- Dose Rate : higher rate → higher instant RIA
- Temperature : lower T → higher instant RIA



Temperature dependence

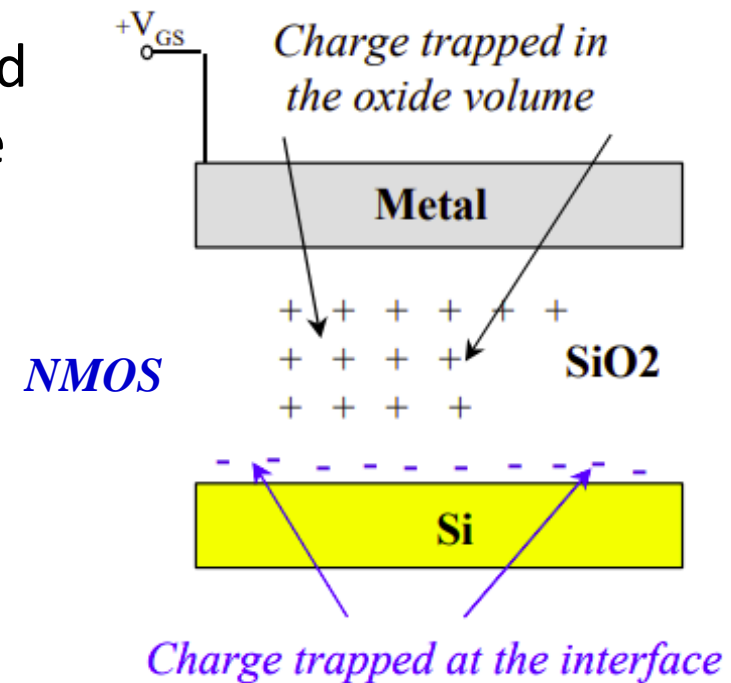
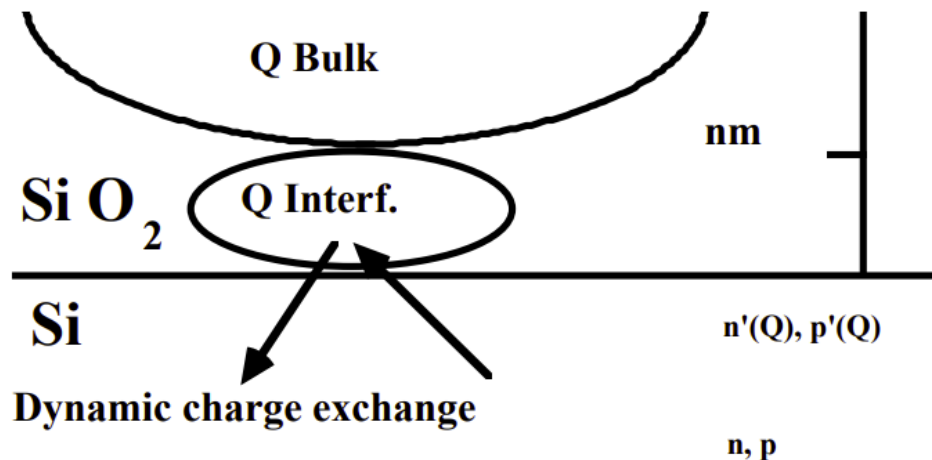
Inst. RIA
10hr Annealed

Dose Rate dependence

TID effects on CMOS

Total Ionizing Dose (TID) induces charge-trapping at **Si-SiO₂ interface**
Dependence : total dose, dose rate, annealing

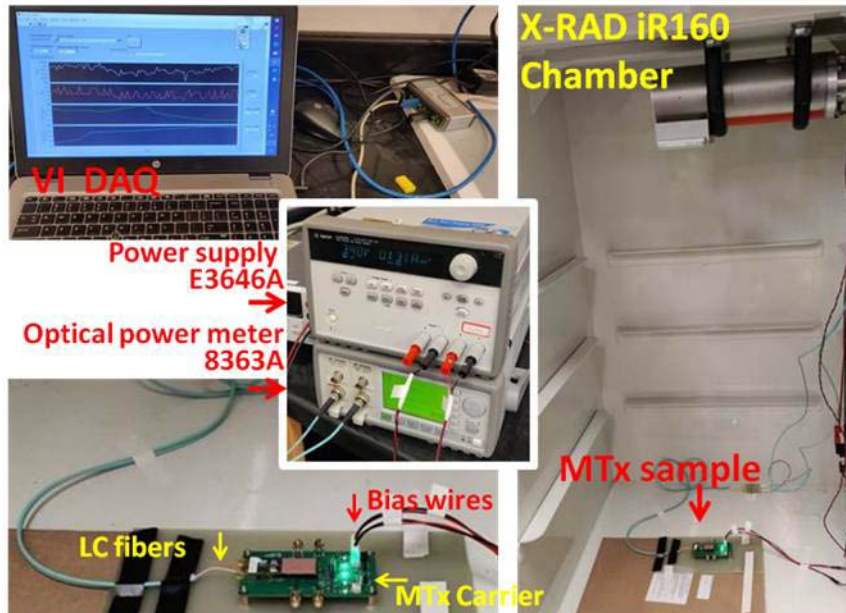
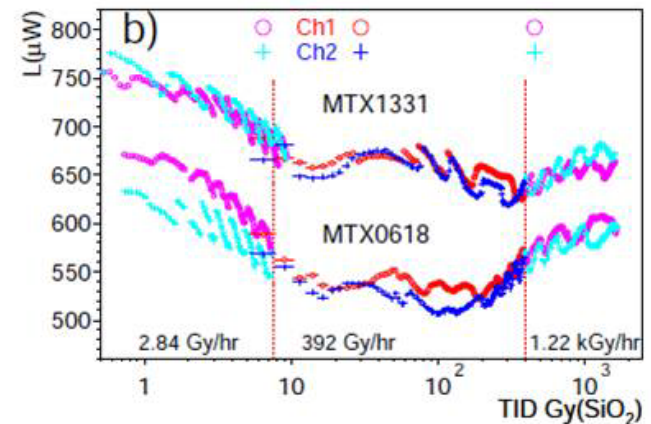
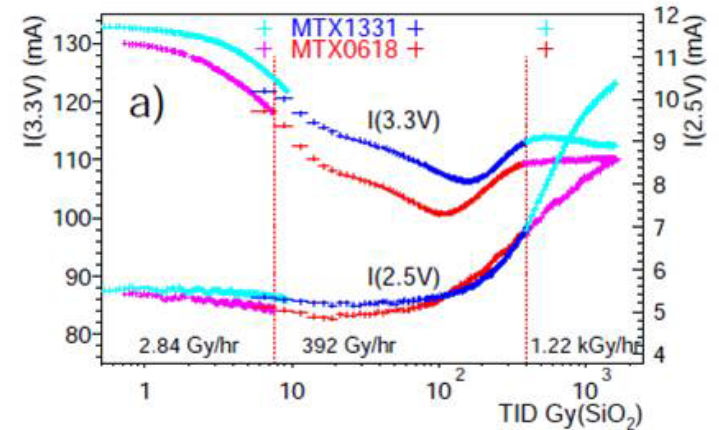
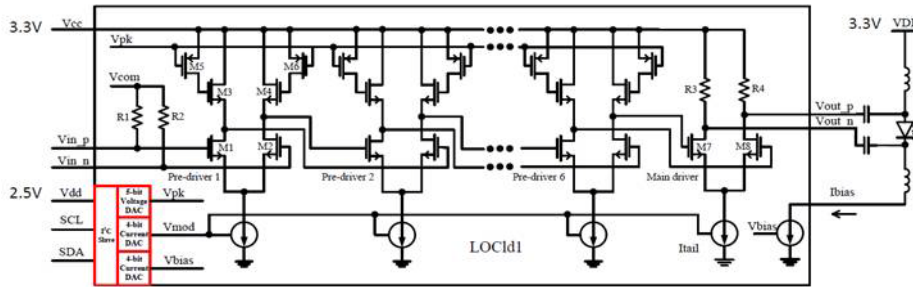
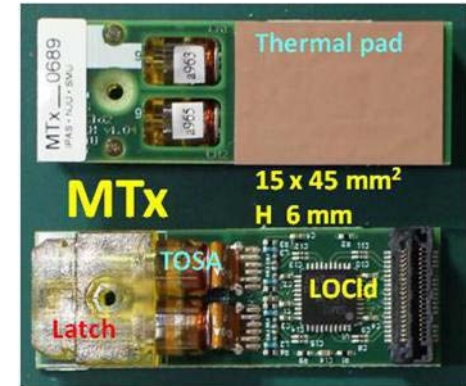
- new interface states formed.
- interface state density vs. energy changed
- interface state density changes with time
- construction after irradiation
- static and dynamic electrical response of the Si-SiO₂ altered



Threshold voltage degradation as a function of the total Dose

X-ray TID on MTX LOCI_d of ATLAS LAr

X-ray, 3 dose-rates in 280 min to 1.62 kGy(SiO₂)
 LOCI_d laser driver shows degradation

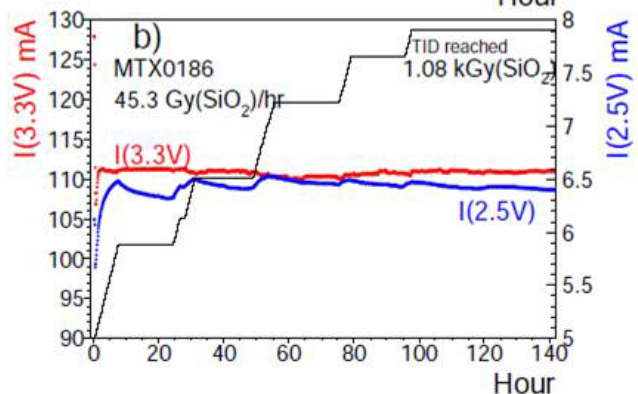
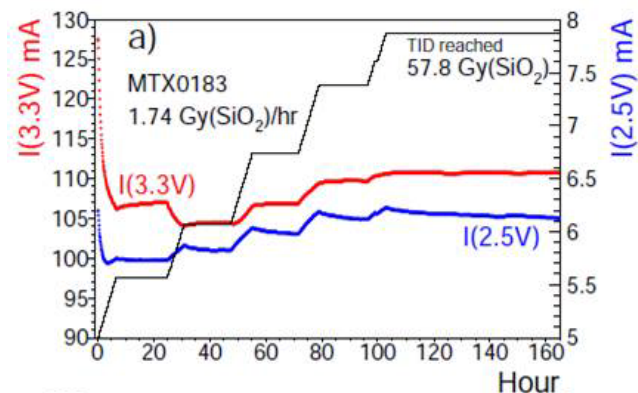
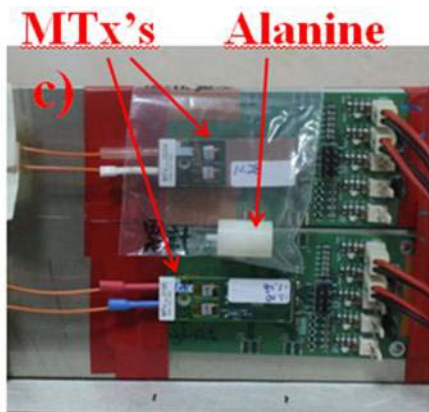


Co-60 TID on MTX LOCI'd

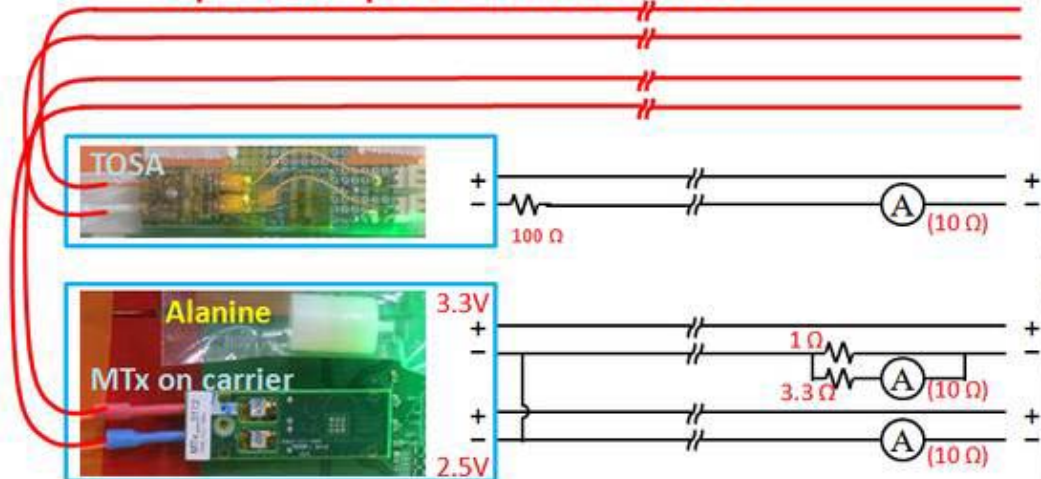
Co-60 varying dose-rates 0.14 to 45 Gy/hr

Irradiation in daytime, annealing overnight

Dose calibration using Alanine



Optical outputs LC fiber cables

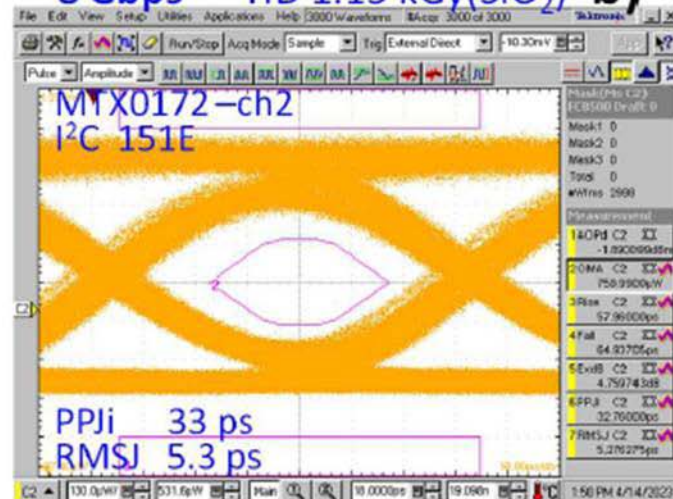
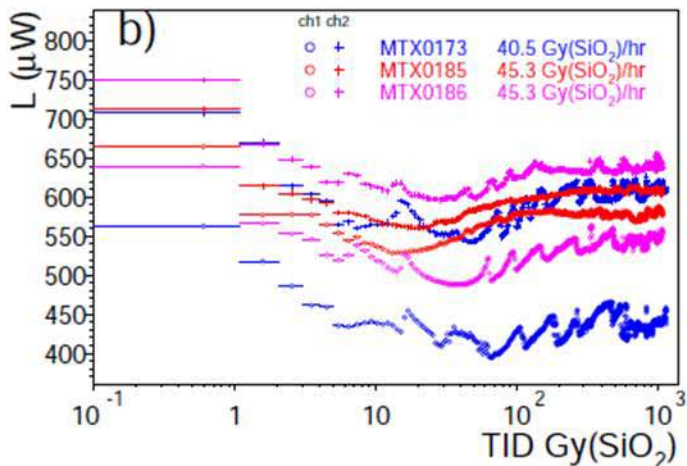
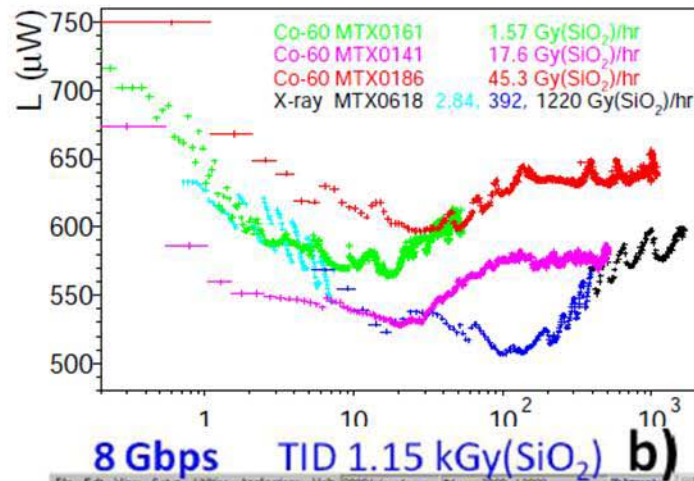
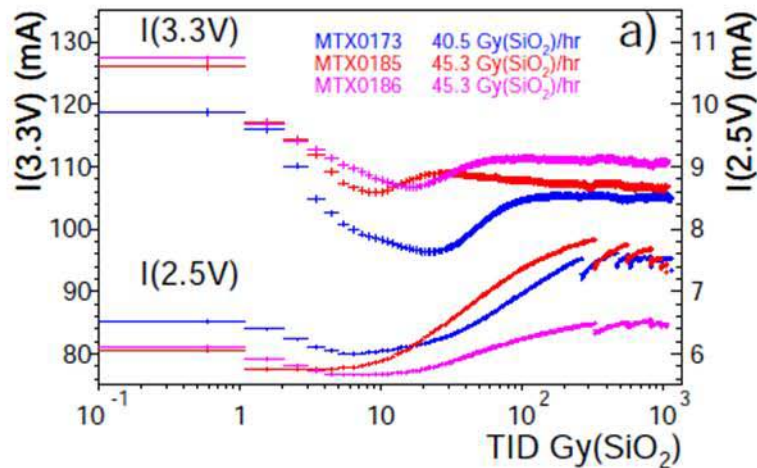
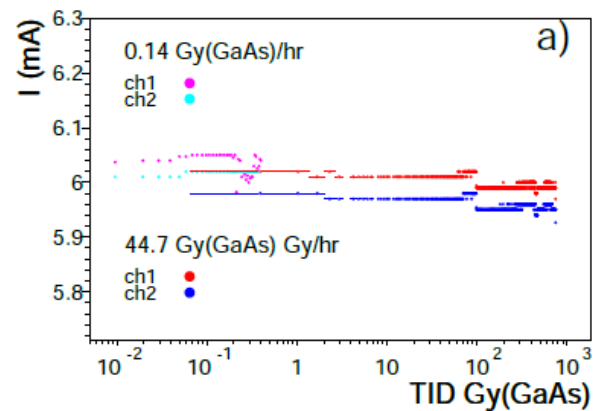


Co-60 TID on MTX LOCLd

Co-60 lower rate to X-ray, mio

VCSEL not effected by TID

CMOS current drops ~ 10-100 Gy(SiO₂)



VCSEL rad-hard degradation, annealing

VCSEL light degradation → linear to fluence

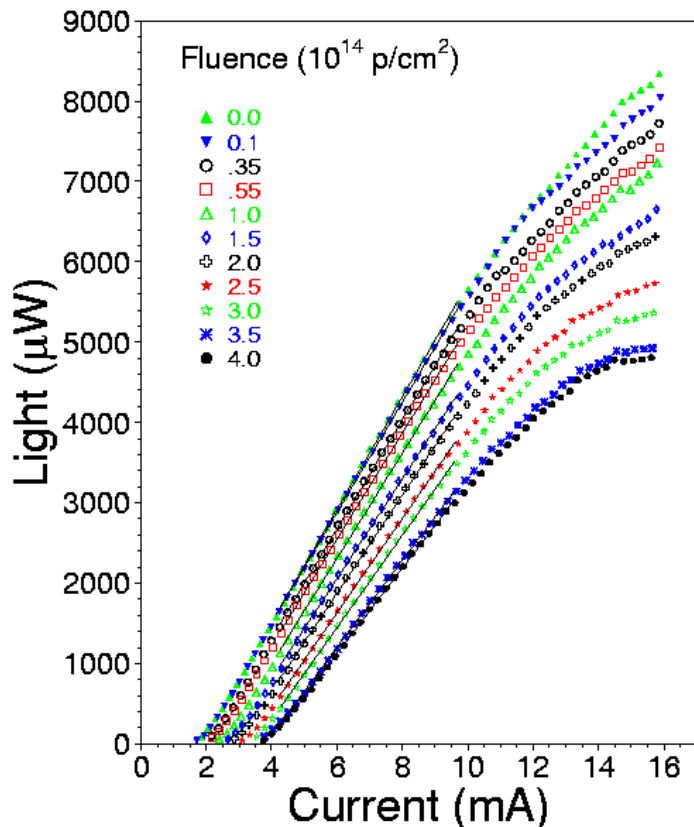
rad-hard fiber connected to readout, independent of flux rate

Fast annealing by charge injection

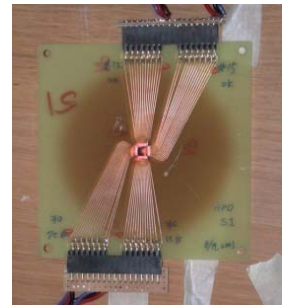
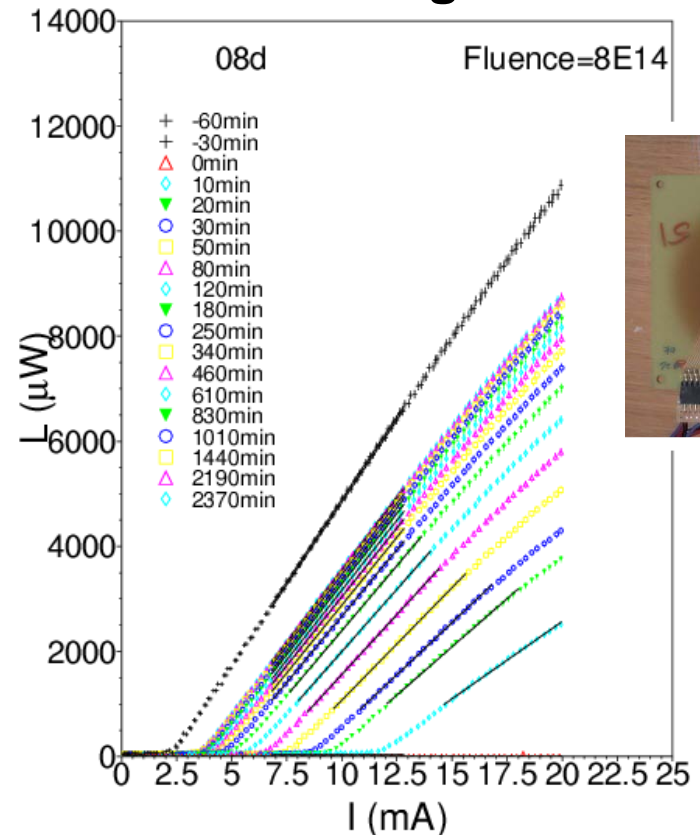
at operation current (10 nA) applied



L-I of VCSEL (oxide)
vs. **online** Fluence



L-I of VCSEL (oxide)
vs. **Annealing** time



NIEL to GaAs diodes

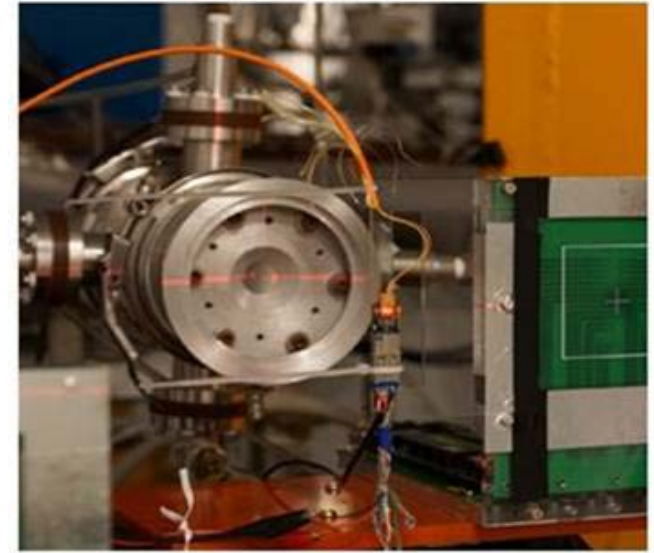
Laser diodes (850 nm VCSELs, 1 Gbps)

NIEL damage

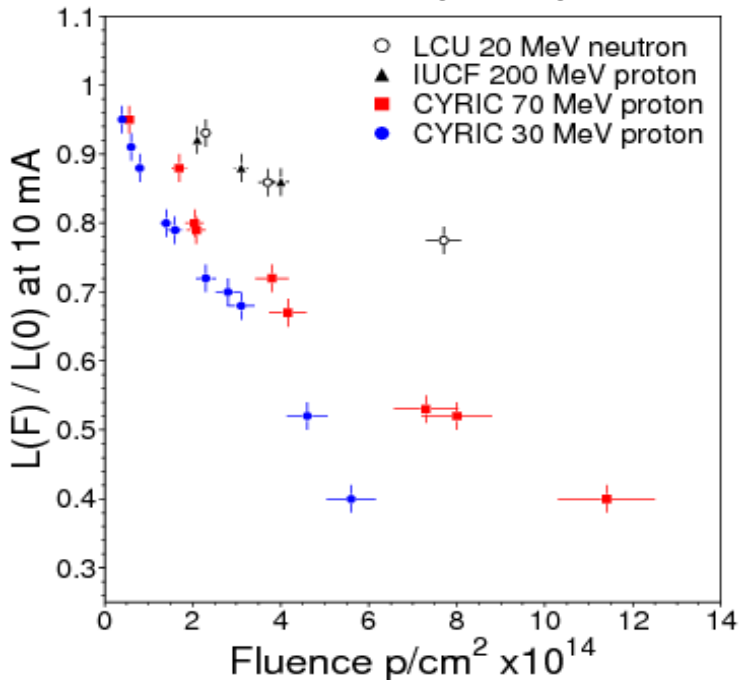
→ light degradation linear to fluence

Higher proton momentum

→ less damage to GaAs components
(contrary to theoretical calculations)

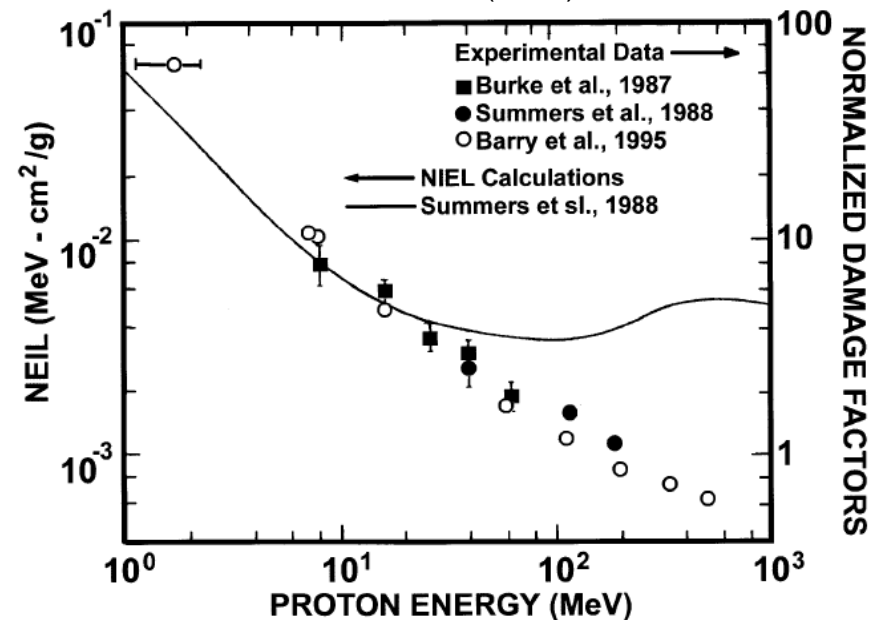


VCSEL (GaAs) light degradation



GaAs solar cell measurements

Srouf, IEEE TNS 50, 653 (2003)



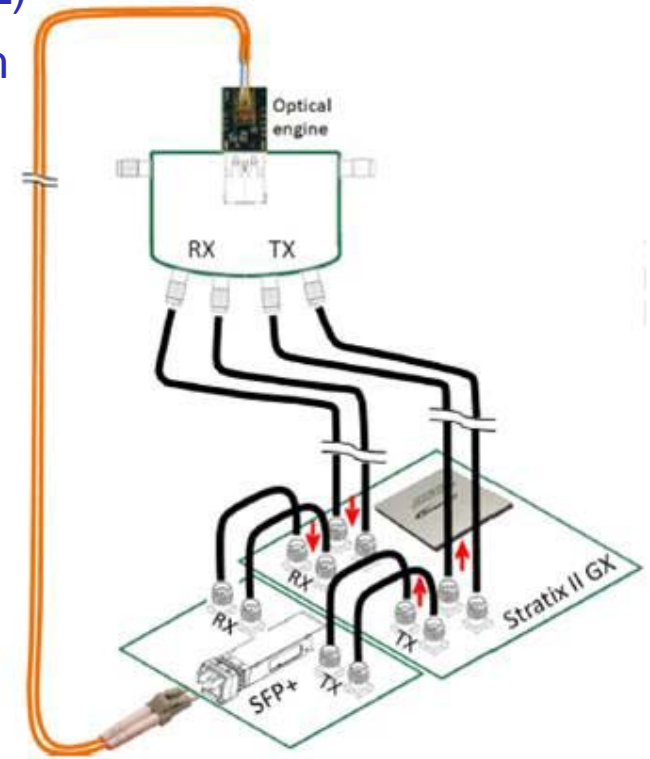
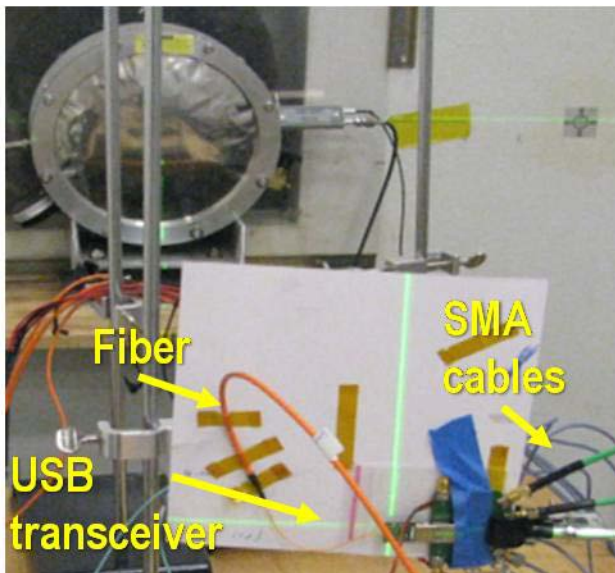
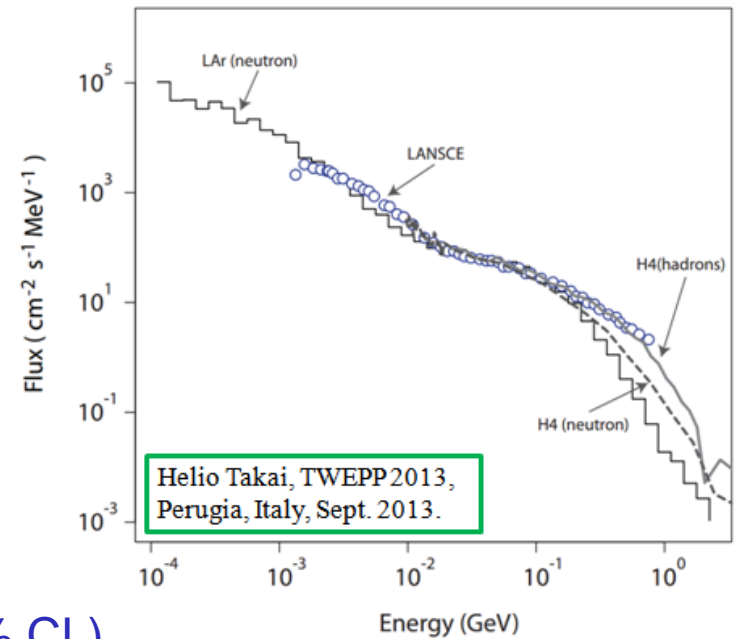
LANSCE neutron test

- Beam profile similar to ATLAS
- **USB transceiver in Bit-Err-Rate**
Straitix II GX, PRBS 2⁷-1 bit pattern
TX path, RX path tested separately

neutron flux 2.9×10^5 n/cm²s
over 1.5 days to 3.8×10^{10} n/cm²

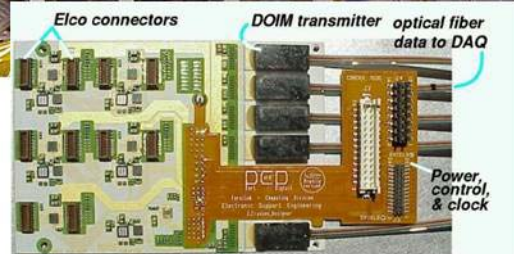
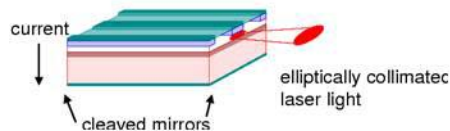
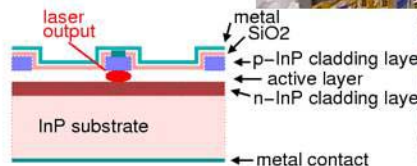
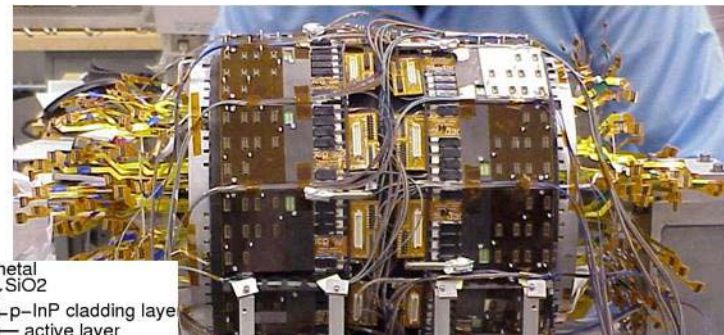
TX: 0 error, upper limit 1.0×10^{-10} cm²/ch (95% CL)

RX: 11 errors SEE cross section 2.9×10^{-10} cm²/ch

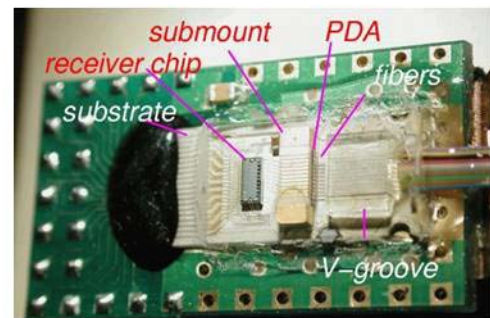
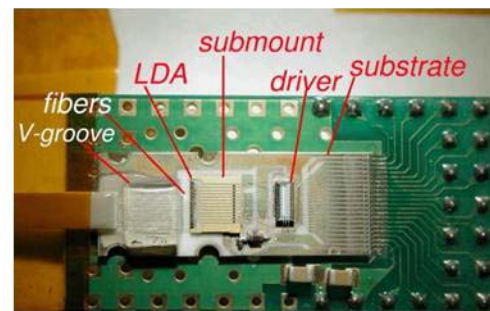
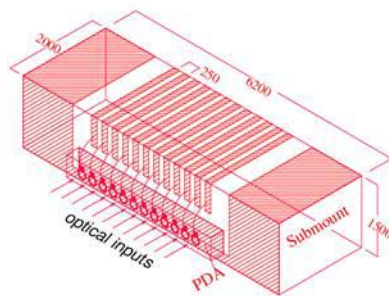
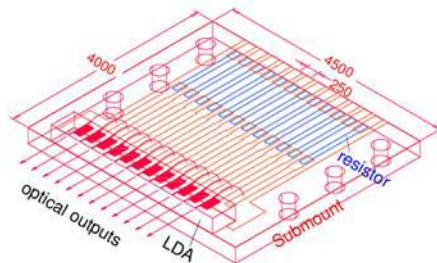
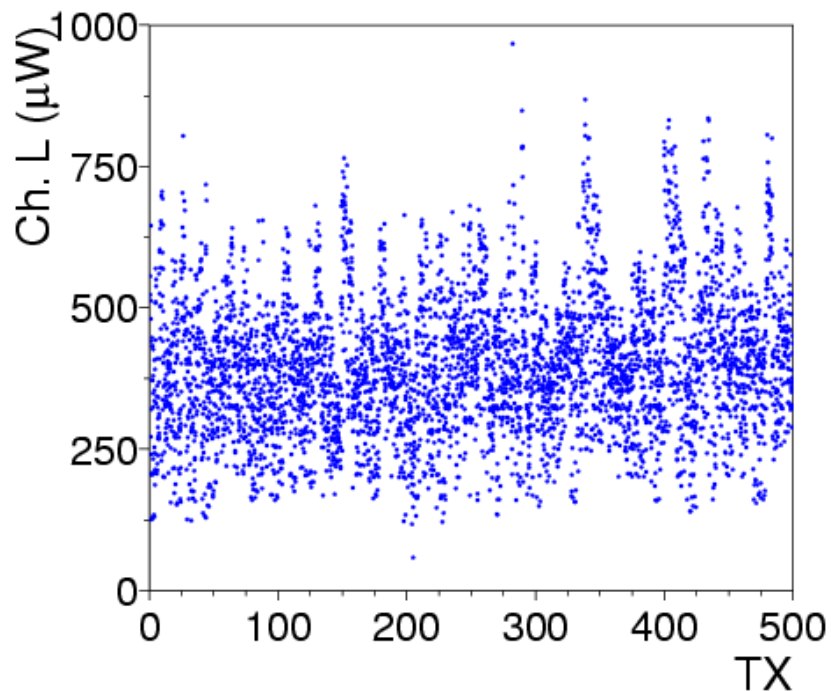


CDF optical link assembly

- 570 modules of Byte-wide parallel links
- Edge-emitting laser diode array
- 22 meter MM Ge doped fiber cable
- 53 Mbyte/sec, BER 10^{-12}



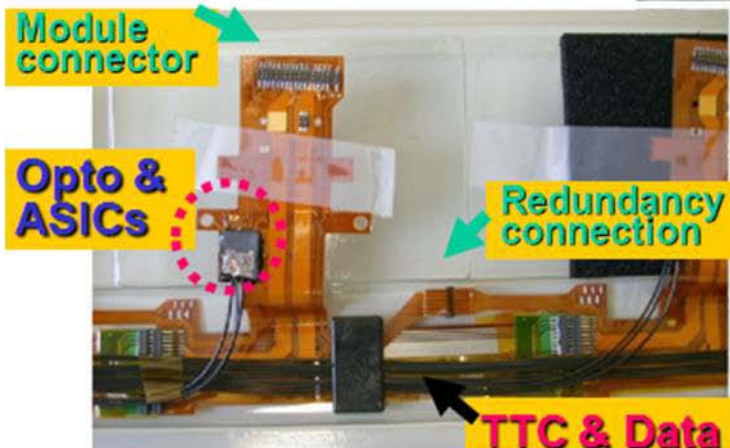
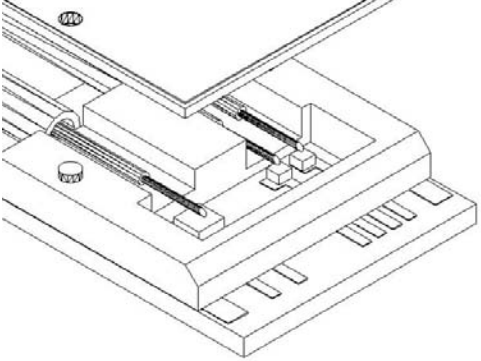
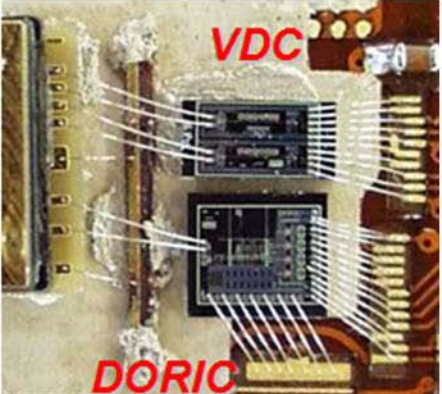
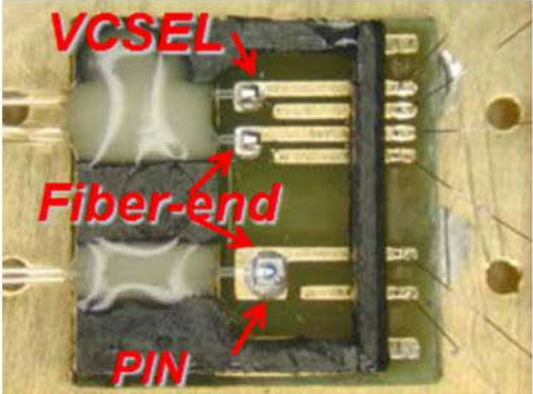
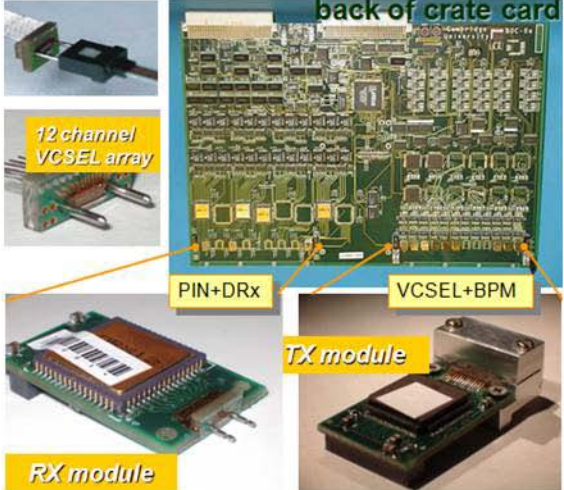
Each column is plotted with light levels of the 8 channels of a module



ATLAS ITk 1st optical link

- VCSEL 850 nm matched to Epi Si PIN diodes.
- low thresholds (~3 mA).
- **Very radiation hard**

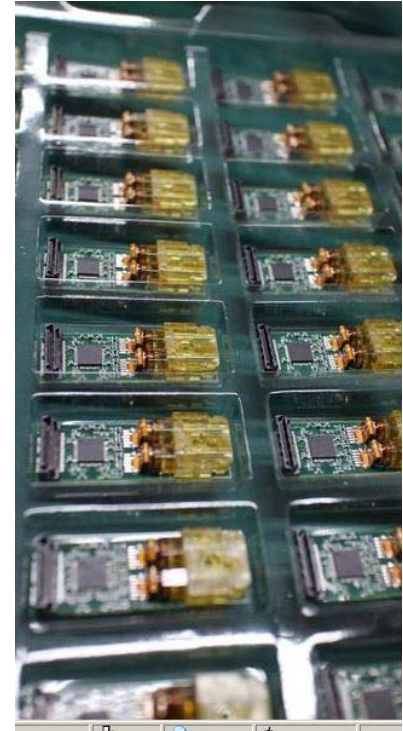
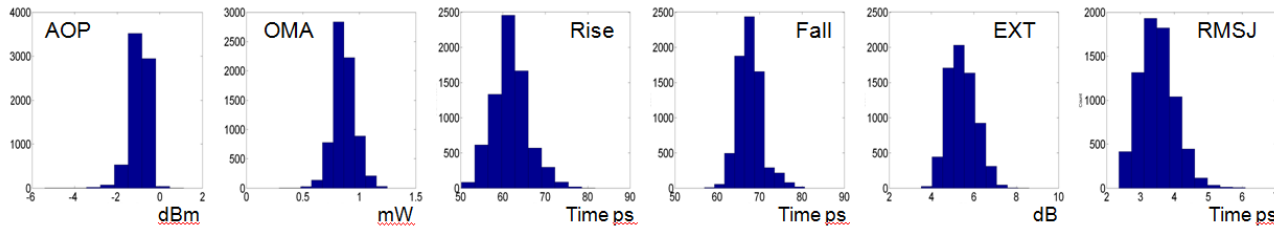
- 1 RX PIN + DORIC
- 2 TX VCSEL + VDC
- 45° fibre-end mirror



ATLAS Phase-I LAr transceivers

- ~ 5k transceivers produced by AS TW opto-electronic companies
- Quality Assurance, Quality Contract at AS: every channel measured for Chip test, TOSA L-I, eye-diagram, Bit-Error-Rate, Burn-in eye-diagram parameters well within specification

MTRx/MTx	AOP	OMA	Rise time	Fall time	EXT	RMJ
Criterion	> -3.5 dBm	>300 μ W	<80 ps	<80 ps	>3dB	<4.5 ps



eye-diagram, BER

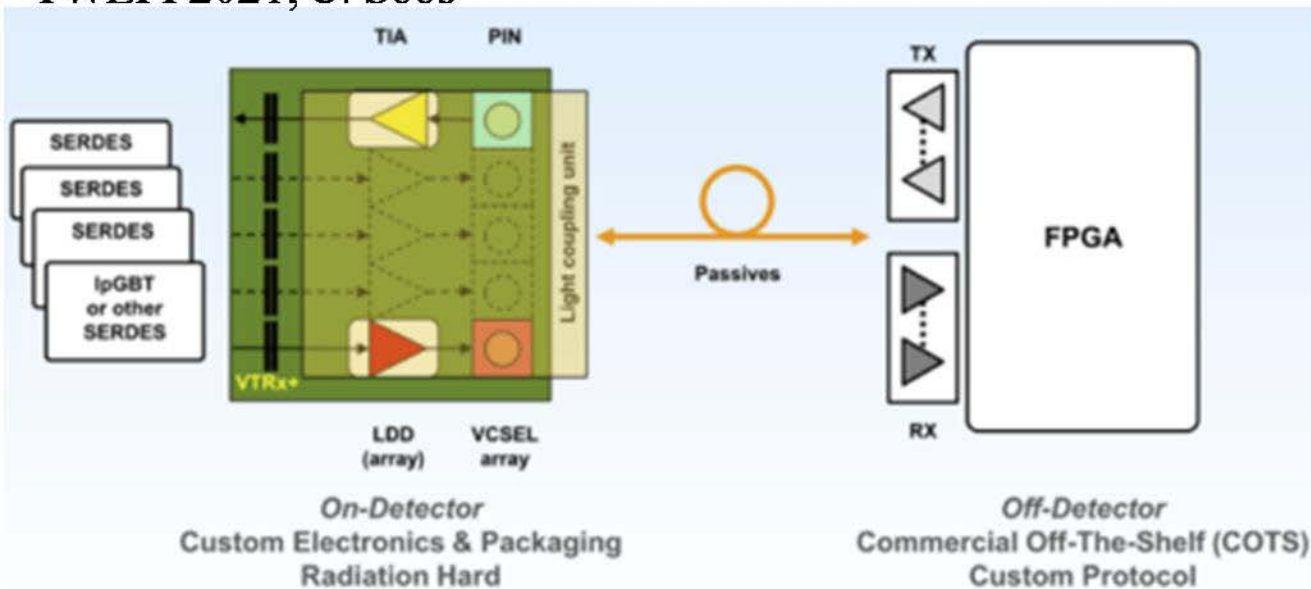


The CERN Phase-II VTRx+ (FOCI 上詮)

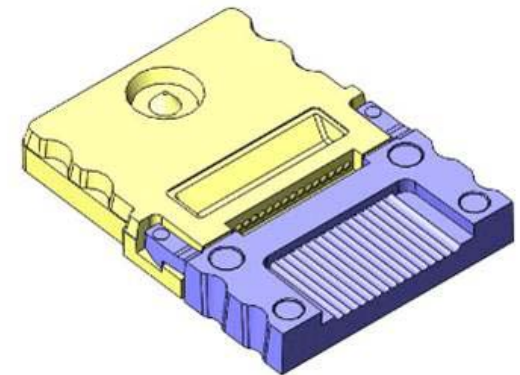
- CERN Versatile Link+ group → one module for all
- 10x20 mm² height 2.5mm, 4.5mm
- 4TX+1RX, 10Gbps TX, 2.56 Gbps RX
- VCSEL array laser driver LQD, TSMC 65nm
- Optical Receiver GBTIA TSMC 65nm
- production 65K pcs
- Lens is the TW Orange-tek



TWEPP2021, C. Soos



Orange-tek
OT-12, OT-13



Light coupling Technology

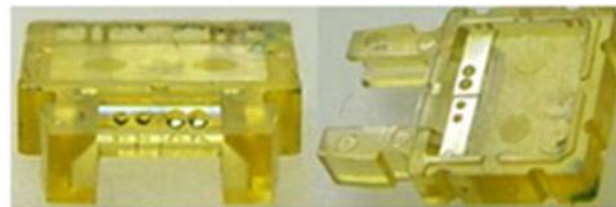
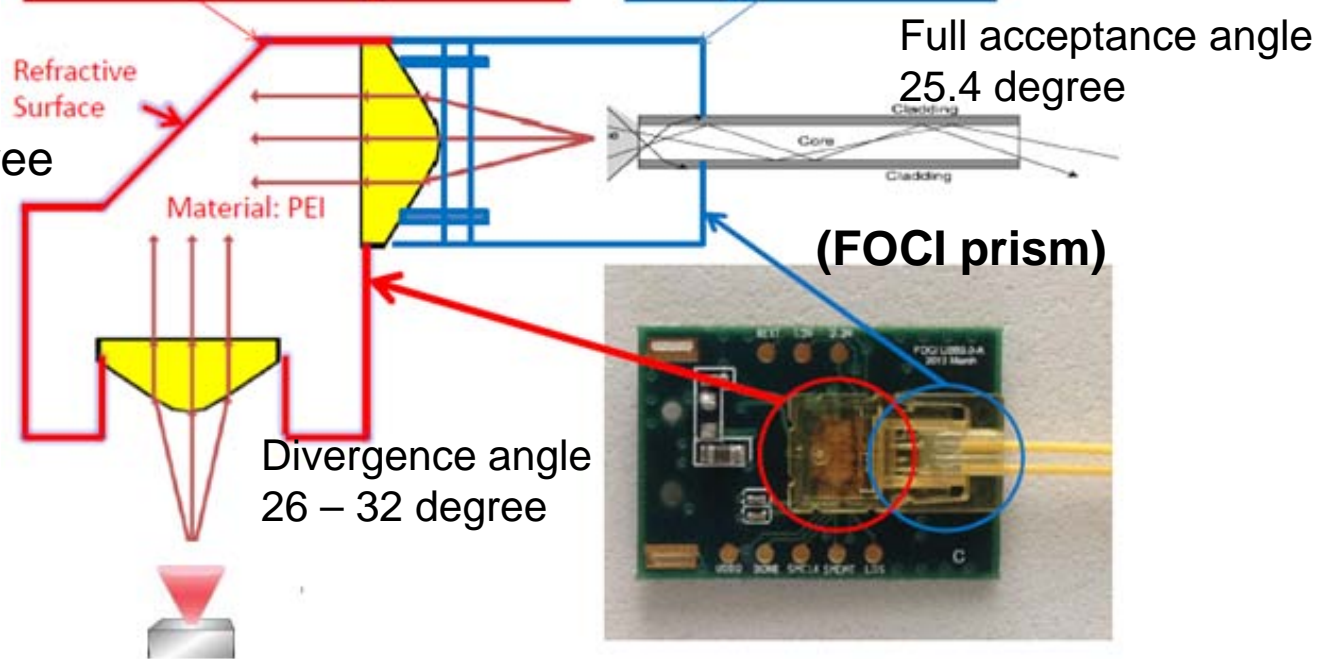
- **Light Peak example, Intel** technology delivers high bandwidth starting at 10 Gb/s to mainstream computing and consumer electronics

- **Lens/Prism :**

Spherical-aberration free
Plano-Convex
Hyperbolic Lens

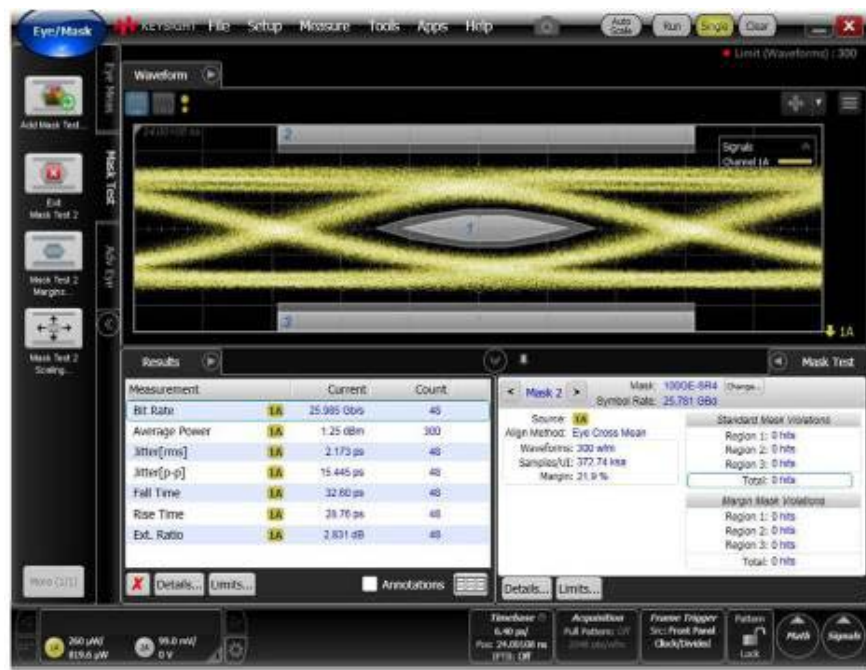
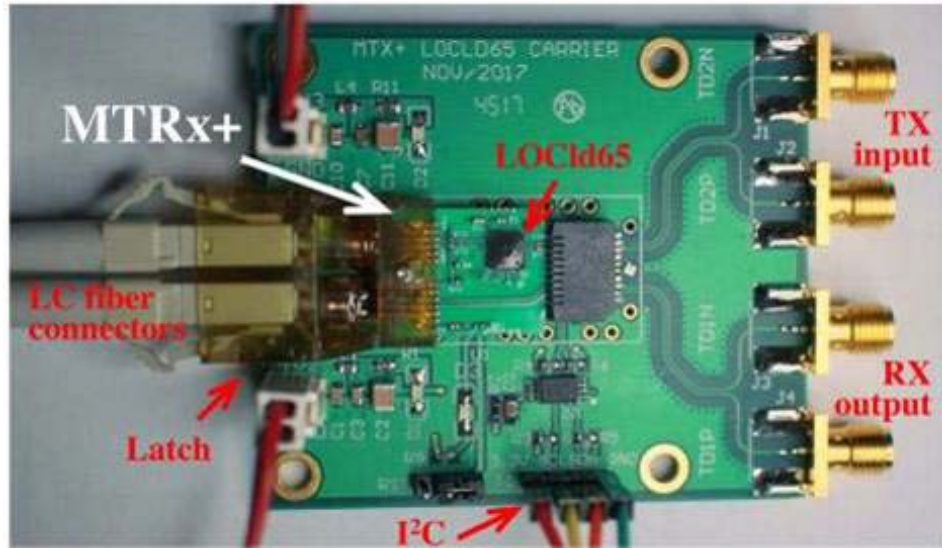
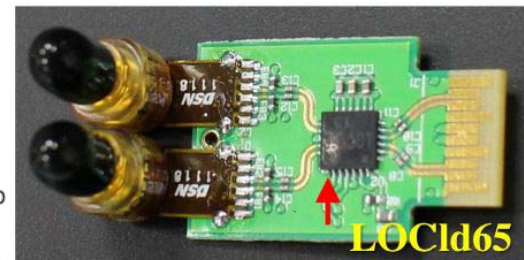
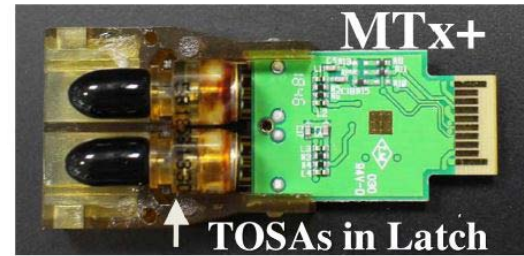
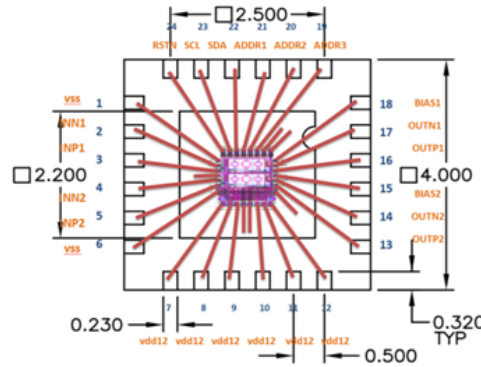
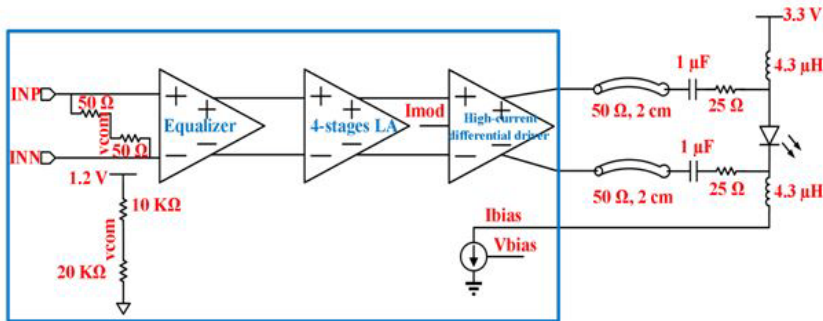
VCSELs aperture
from t Ø 5 to 20 µm

Prism Receptacle + Plug Ferrule



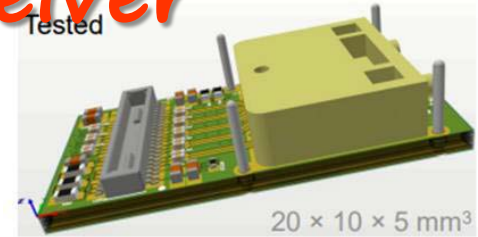
AS+SMU+前鼎: 25 Gbps Transmitter

- 25 Gbps components, PCB, connectors
- Driver, LOClD65, TSMC 65nm



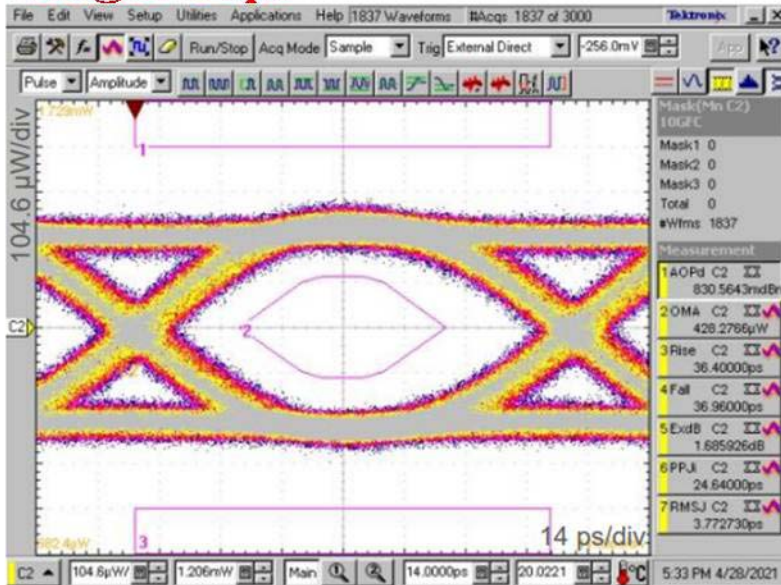
AS+SMU+前鼎 QTRx Transceiver

- 4TX+4RX, tested @10G, will do 25 Gbps
- VCSEL, PD, PCB all 25 Gbps qualified
- Driver, QLDD, QTIA, TSMC 65nm

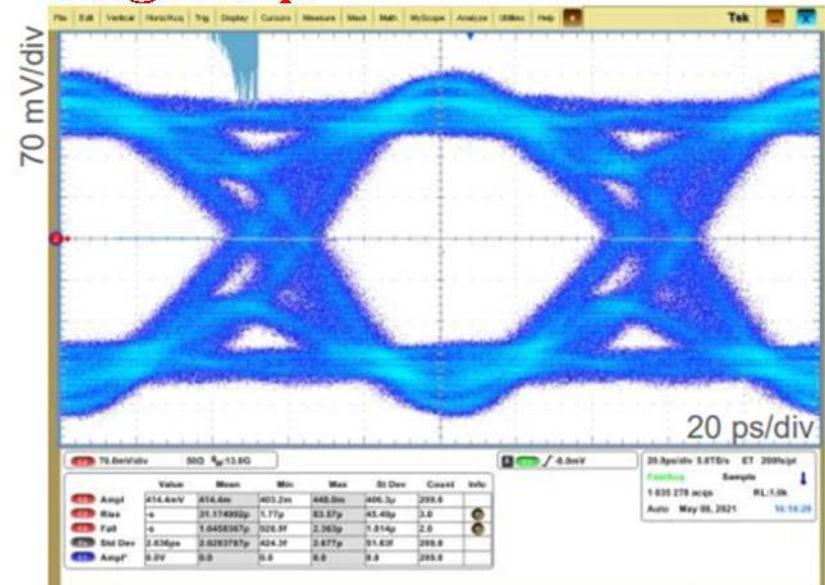


QTRx	QLDD	QTIA	
Data rate	10 Gbps	2.56 Gbps	10 Gbps
Power supply	1.2 V and 2.5 V		
Sensitivity (BER =1E-12)	80 mV	-17 dBm	-8 dBm
Rise /fall time	37 ps	40 ps	50 ps
Total jitter (BER =1E-12)	-	38.5 ps	52.4 ps
Power consumption /ch	124 mW	120 mW with CP	

TX@10Gbps



RX@10Gbps



Summary

- Opto module assembly is mature industry
ASIC packaged, wire-bonding
Electric connectors: SFP, QSFP, Hirose, Firefly
Optical coupling: MT, pigtail mini-formfactor
- TSMC 65 is 25 Gbps ready
SIMC 55 re-freshing, 25 Gbps expected
- Radiation hardness
NIEL on 25 Gbps VCSEL, PD
TID, SEE on CMOS