Simulation of the Electrical Performance of the ITk ATLAS18 Test Diode and Main Sensors Before and After Irradiation

The 14th international "Hiroshima" Symposium on the Development and Application of Semiconductor Tracking Detectors (HSTD-14)

Y. Zhao⁽¹⁾, J. Dandoy⁽¹⁾, C. T. Klein⁽¹⁾, J. Keller⁽¹⁾, T. Koffas⁽¹⁾, E. Staats⁽¹⁾, V. Fadeyev⁽²⁾, M. Ullan⁽³⁾, Y. Unno⁽⁴⁾

(1) Carleton University

(2) Santa Cruz Institute for Particle Physics

(3) Instituto de Microelectronica de Barcelona (4) High Energy Accelerator Research Organization (KEK)

The ATLAS Inner Tracker (ITk) Strip Detector

The ATLAS Inner Detector upgrade with Inner Tracker (ITk) to accommodate higher occupancy and radiation level anticipated at the High-Luminosity LHC (HL-LHC).

- The ITk consist of silicon-based pixel and strip sub-detectors.
- The strip sub-detector is designed to operate up to fluence of 1.6×10^{15} 1-MeV neq/cm².

The ITk Strip Detectors:

- **n**⁺-in-**p** silicon strip sensors.
- ~75 μm strip pitch and approximately ~300 μm active thickness.
- Each sensor contains 2 or 4 rows, with approximately 1k strips per row.
- Fabricated by Hamamatsu Photonics (HPK).

Sensors Data Used in Tuning TCAD Simulation

Test Diodes:

- Fabricated on the **same wafer** as main strip sensors.
- Include optical and electrical characterization data.
- Extensive QC data collected during reception acceptance measurements.
- Additional in-lab measurements at Carleton for TCAD model studies.

Main Strip Sensors:

 Measurements of main sensors (R0/2/3) at Carleton to validate and compare TCAD simulation results based on models tuned with the test diode data.

TCAD Simulation of ITk Strip Sensors

Simulation Setup

- Models and Parameters:
 - Doping and passivation fixed charges estimated from C-V measurement and implant resistance.
 - The **bulk defect model** is based on **DLTS measurement** of non-irradiation sensors.
- Test Diodes:
 - Simulated as half-size 2D structure to exploit symmetry and reduce computation time.
 - Electrical simulations use cylindrical boundary condition to reproduce the effective sensor area.
- Main Sensors:
 - Simulation include the edge structure and a few strips, then results are rescaled to full sensor size.

Pad Test Diode Edge Strip **Main Sensor**

Initial Attempts

- An additional area scaling 0.06 is applied to match the simulated leakage current to the measurement value.
- The excess current still under investigation and may be due to inaccurate effective area.
- The simulated leakage current increase faster than measured before full depletion.

Important Models:

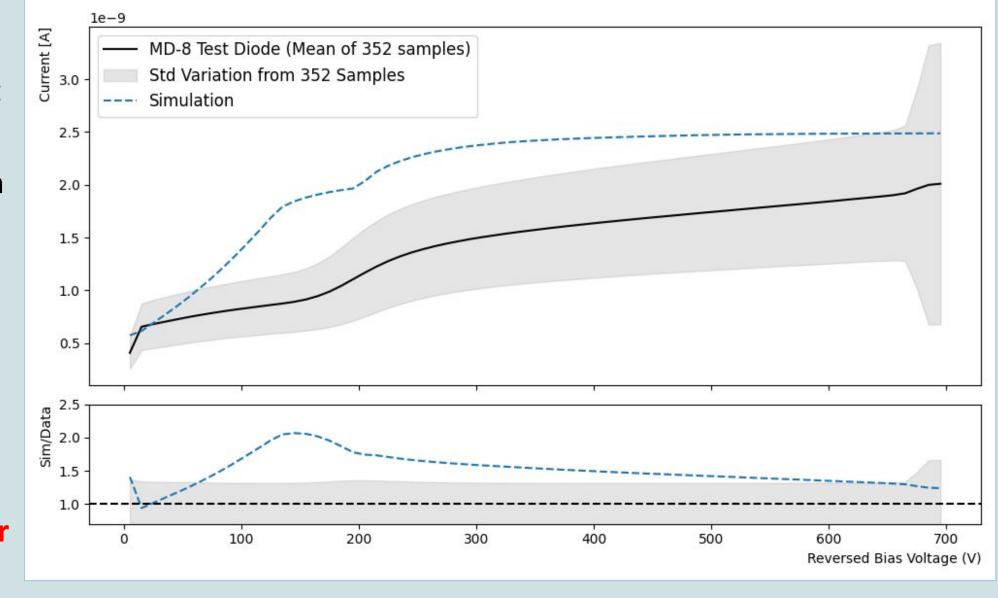
charge density: 10¹¹ /cm² at the SiO₂-Si interface. [1]

Uniform passivation oxide

- Bulk defect model: Based on **DLTS** measurements. [2].
- SRH recombination lifetime (e/h): 200ms.

Large discrepancy observed between TCAD and measurement

⇒ Current models may be incomplete and require further tuning.



Separating Bulk and Edge Leakage Contributions:

- The total leakage current can be decomposed into bulk (pad) and surface (edge) with edge contribution dominates the total current
- Help evaluate the impact of different models on each contribution.
- In the TCAD simulation, the guard ring is grounded to allow extraction of bulk and edge currents independently.
 - Note this is different from nominal operation of the device, where GR is floating.

Acknowledgments:

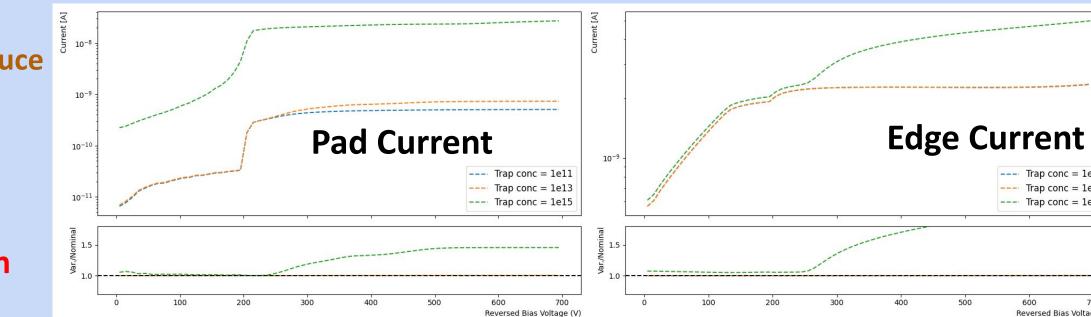
The work was supported by the Canada Foundation for Innovation under project number 36248 with additional resources were provided by the Natural Sciences and Engineering Research Council of Canada; the Spanish R&D grant PID2021-126327OB-C22, funded by MICIU/ AEI/10.13039/501100011033 and by ERDF/EU; the US Department of Energy, grant DE-SC0010107; the European Structural and Investment Funds and the Ministry of Education, Youth and Sports of the Czech Republic via projects LM2023040 CERN-CZ, and FORTE - CZ.02.01.01/00/22_008/0004632.

TCAD Model Tuning to Test Diodes

Backside Trap Effects

- A kink observed in the measured I-V curve corresponds to the **onset of full** depletion in the sensor.
- This behavior is linked to how the depletion region extend toward the backside as the bias voltage increase.
- To reproduce the kink simulation, **traps** with Gaussian spatial distribution near the backside were attempted.

Trap parameters are inferred from the Perugia



surface model [3]. However, the traps introduce

higher leakage after depletion, with the kink before depletion remains unaffected by the traps.

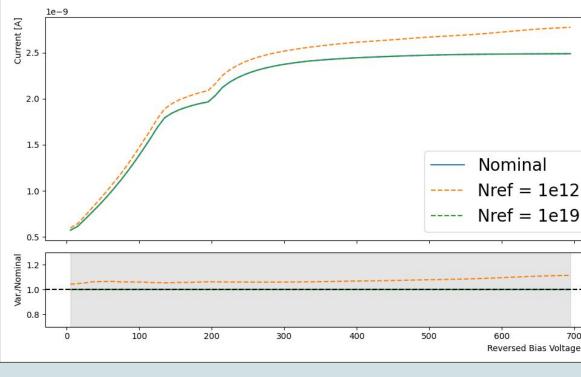
⇒ Further optimization under study.

SRH Recombination Lifetime

- The doping dependence of the SRH lifetime is modeled as: $\tau_{dop}(N_{A,\,0}+N_{D,\,0})=\tau_{min}+1$ • The T_{max} = 200 ms (typically for high-purity silicon).
- The $T_{min} = 0$ ms (nominal model value).
- The γ parameter is set to 1.0 (nominal model value).
- The reference doping concentration N_{ref} is varied: • 1e12 N/cm³ (bulk doping level)
 - 1e19 N/cm³ (surface implant doping level)

Increasing the reference doping concentration to the implant level yields results similar to the nominal case, while reducing to the bulk level slightly increase the current. ⇒ This indicates the SRH lifetime doping dependence has minimal effect on the edge current contribution.

Surface SRH effects will be investigated in the future studies.



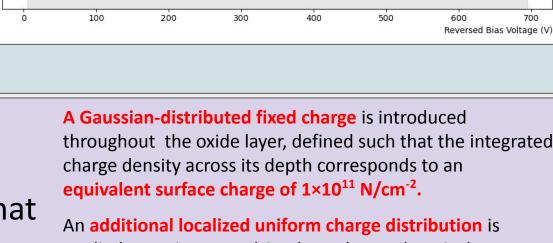
Total Current

Compared to measurements

 $\tau_{max} - \tau_{min}$

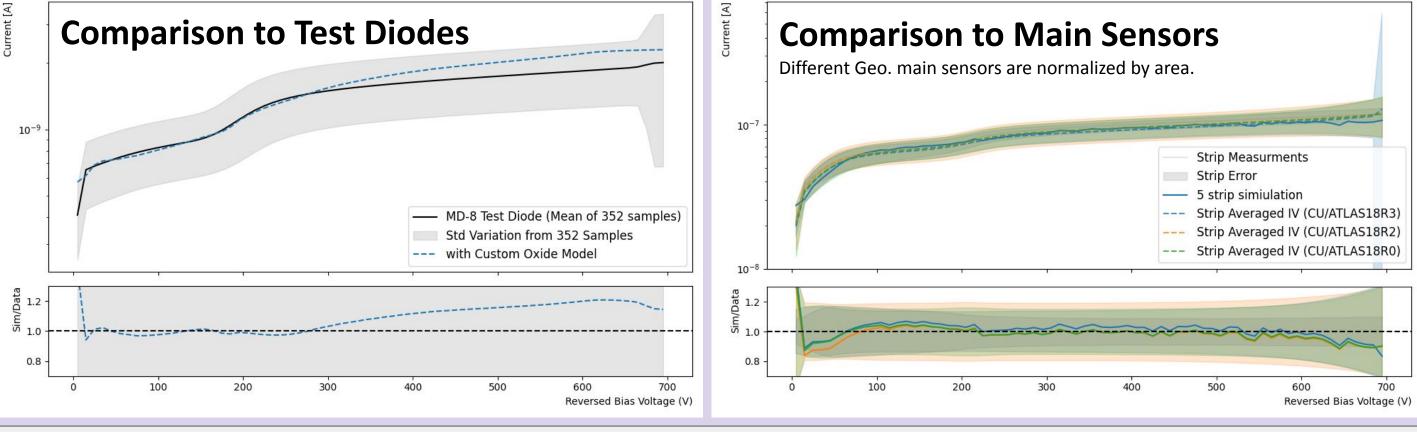
Custom Oxide Fixed Charge Modeling

- Analysis of the bulk and edge current contributions shows that the edge current dominates the total leakage.
- To account for this, an additional fixed oxide charge with **spatial** distribution is introduced in the peripheral regions.
- Such non-uniform oxide charge distributions are plausible due to fabrication process variations.
- Hysteresis observed in the inter-strip capacitance measurement suggests the presence of these oxide charge variation [4].





Test Diode



Summary:

Conclusions

- Dedicated TCAD model tuning is essential to ITk strips to reproduce the electrical measurements.
- The custom oxide fixed-charge modeling shows good agreement with both test diodes and main sensors measurement up to full depletion, with slightly higher leakage current beyond full depletion. An overall current normalization factor remains under investigation.

Future Development:

- Investigate the **origin of the overall excess current** observed in TCAD simulation.
- Study the impact of additional physical models on the pad and edge current contributions, including tuning surface SRH model.
- Implement irradiation-induced defect models based on recent DLTS measurement from the latest irradiation campaign.

References:

- [1] E. Staats, "S Capacitors for QA", ITk Strip Sensor Meeting 1015340 (2021)
- [2] C. T. Klein, "Production MD8 diode (unirrad.) DLTS results", ITk Strip Sensor Meeting 1170857 (2022)
- [3] P. Asenov et al. "TCAD Modeling of bulk radiation damage effects in silicon devices with the Perugia radiation damage model". NIM A 1040 (2022) [4] C. T. Klein, "Investigation of performance and the influence of environmental conditions on strip detectors for the ATLAS Inner Tracker Upgrade", Ph.D. Thesis, University of Cambridge, 2019







