




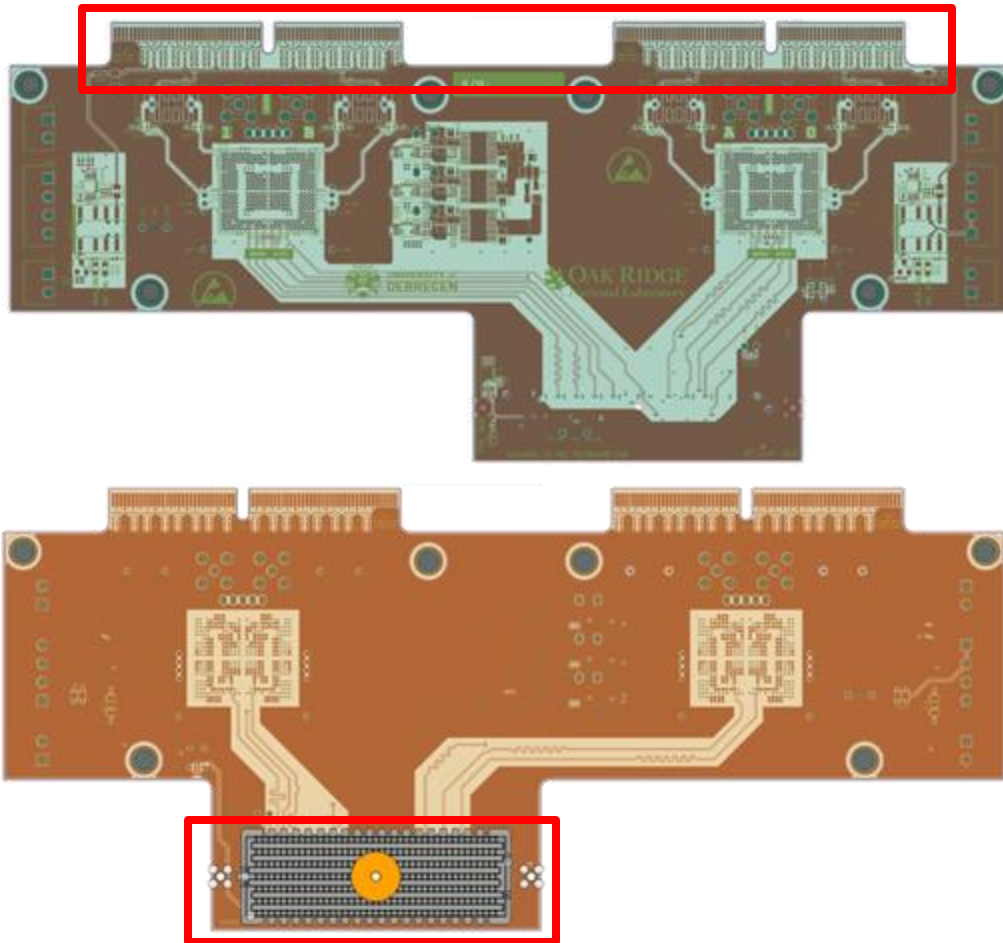
# Status Report

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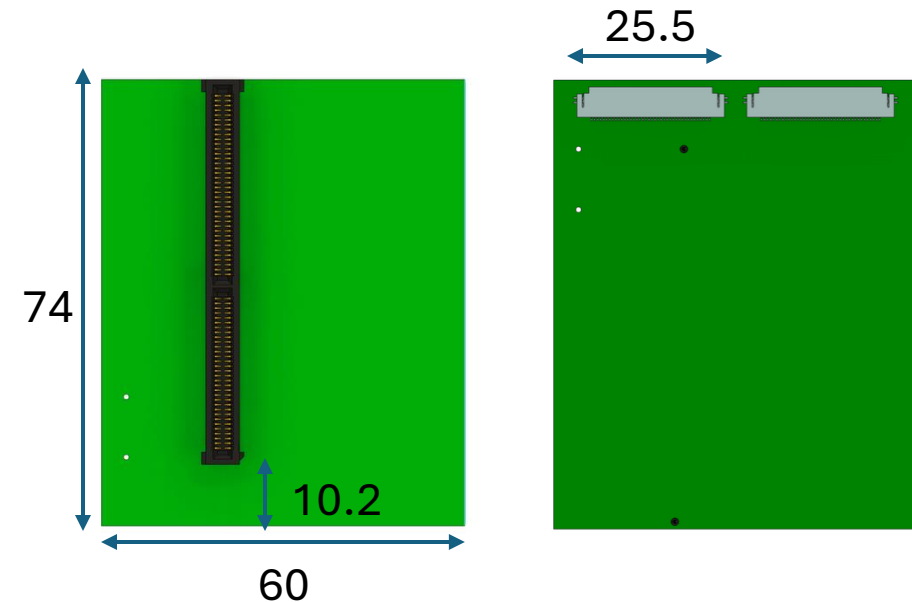


# Adapter

The document H2GCROC of don't have the the size of the **connector**.



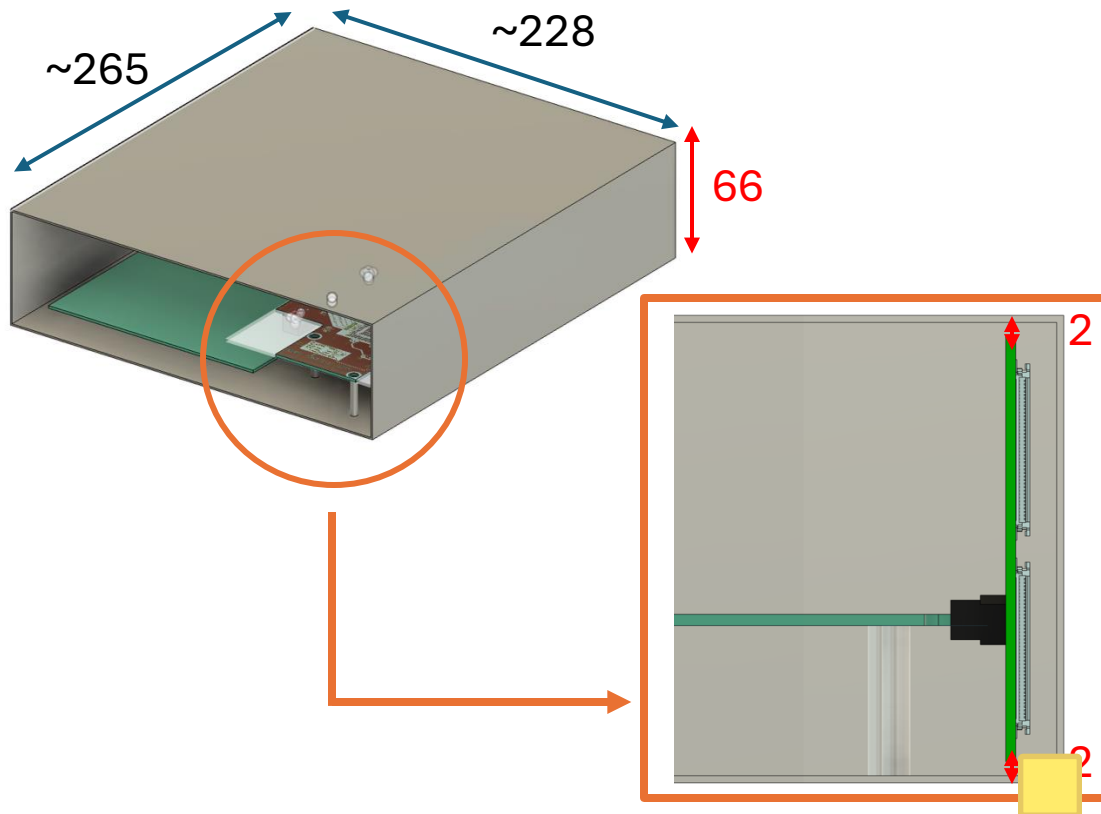
- Adapter connect to H2GCROC separate.
- Avoid getting stuck and installation failure



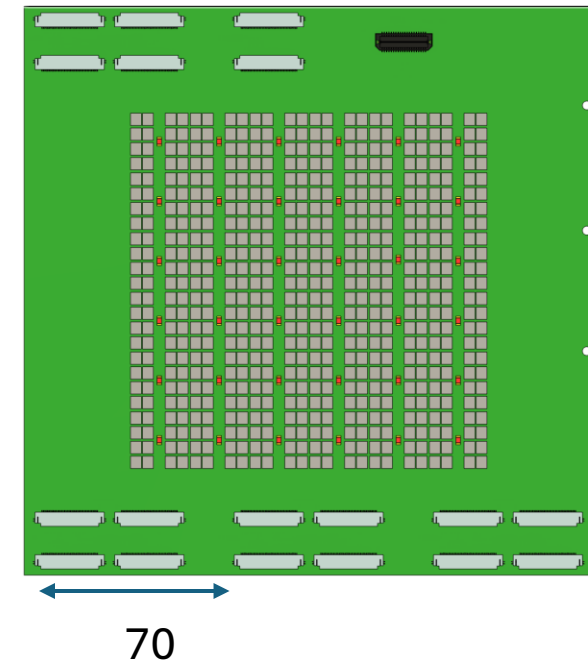
Unit: mm

# Readout Board

- The height of the shielding box is 66mm.
- The gap between the adapter and both sides of the box is 2mm.
- The distance between each layer of the readout board is 70mm.



The connectors are not on the other side?

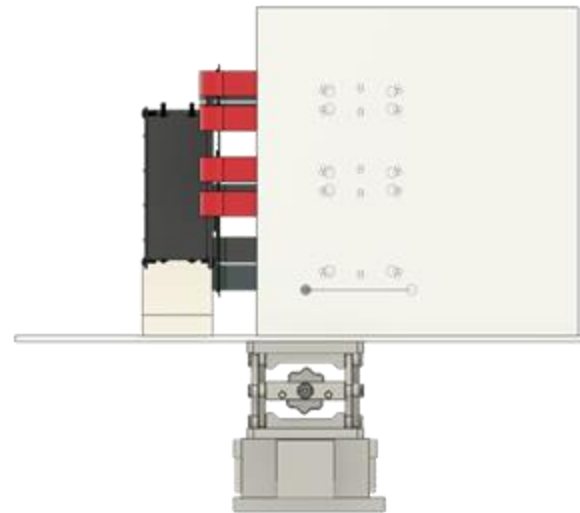
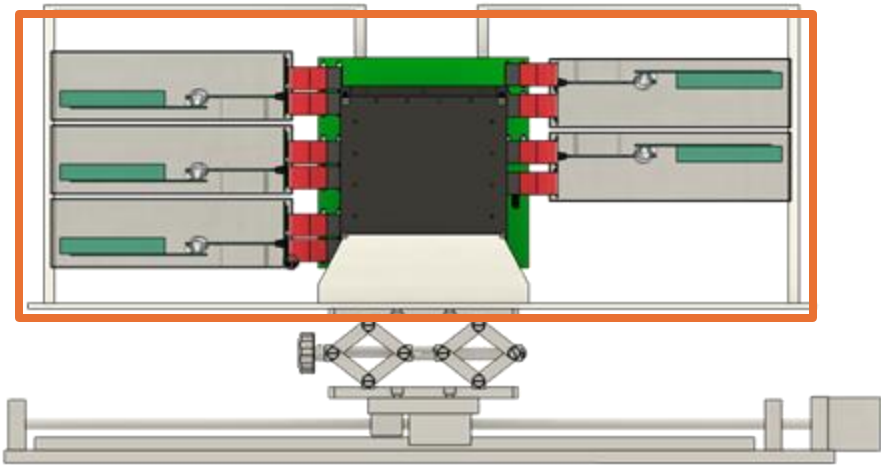


Unit: mm

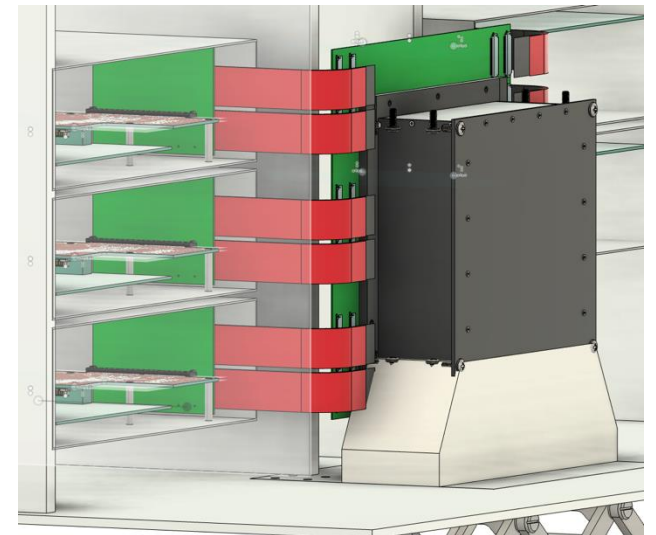
# Structure

The weight of main structure is ~35kg, but the situation of BM need to check.

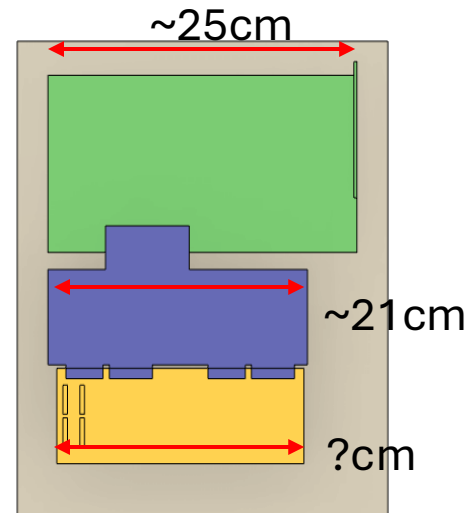
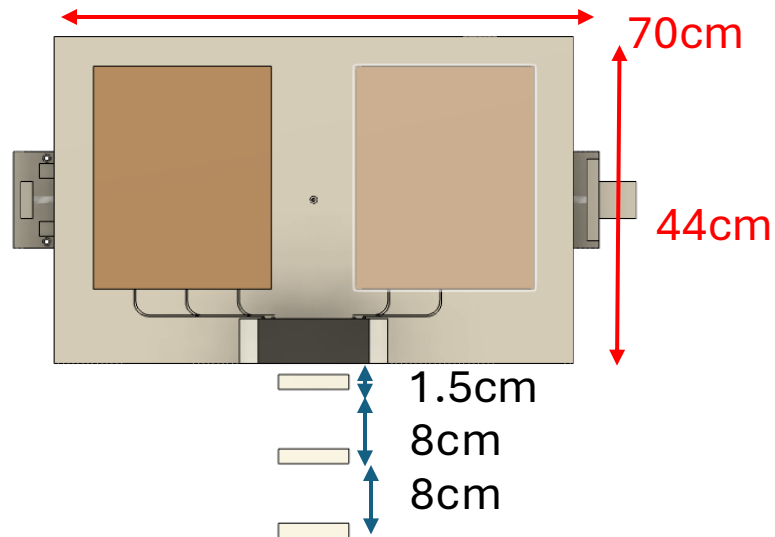
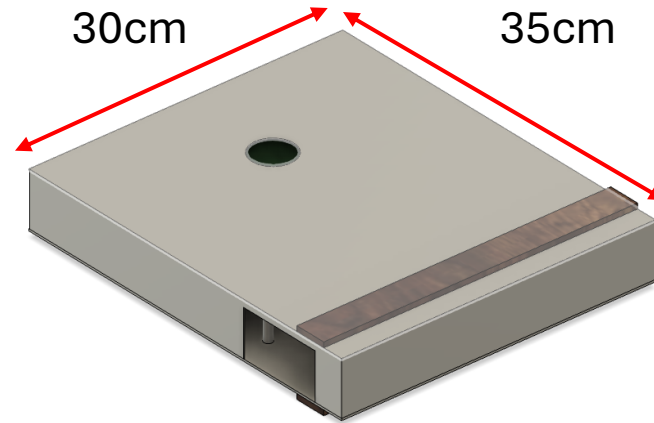
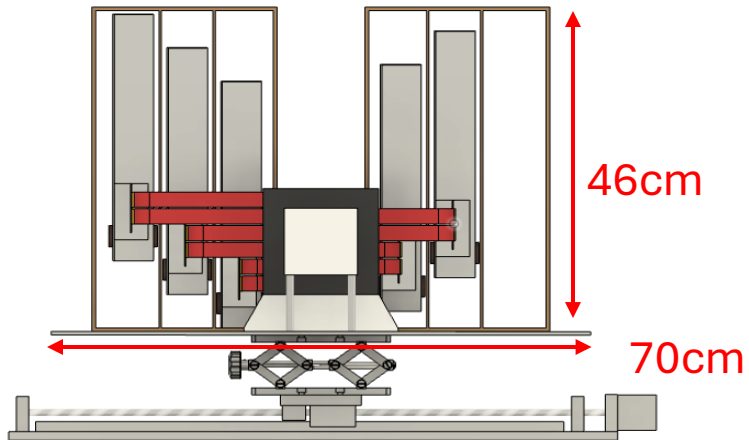
~35kg



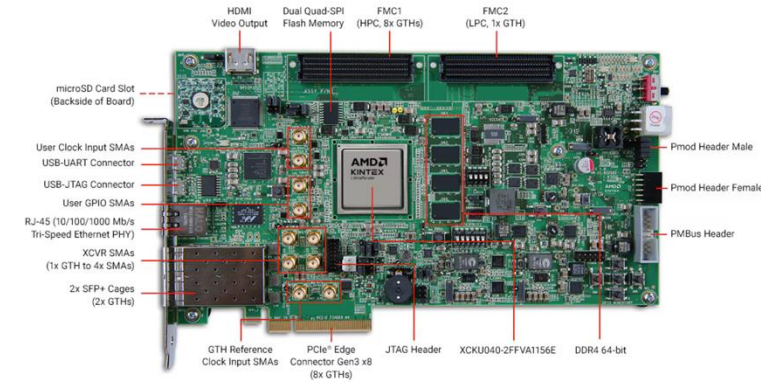
The length of the flat cable in design is ~120mm and ~220mm



# Backup 8/28 Design

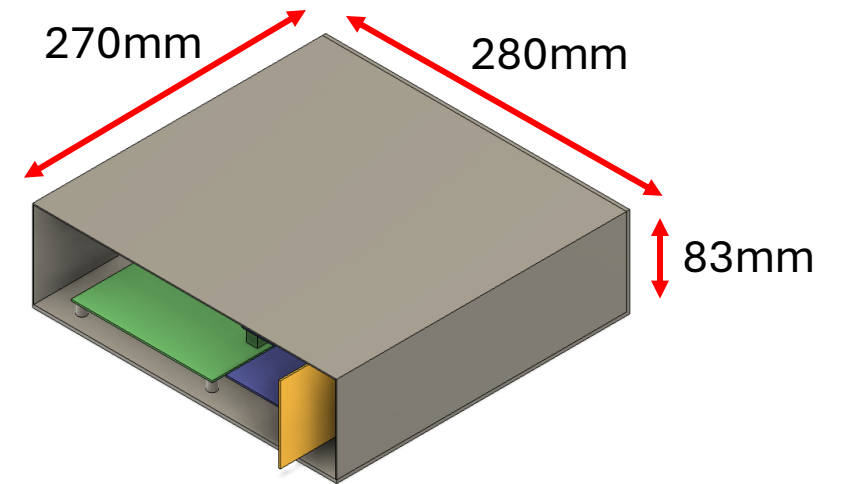
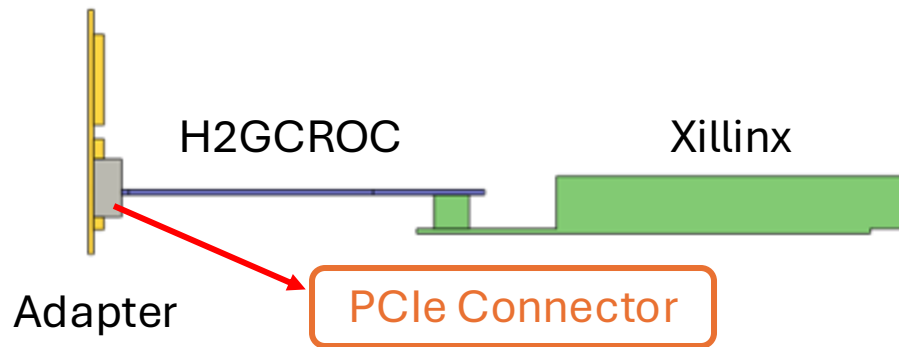


FPGA: EK-U1-KCU105-G



# Readout Board

- The adapter board PCIe connector only can set on the top.  
→ The adapter board is perpendicular to the H2GCROC



- 凱聿 choose the 40 channels flat cable for the detector.
- The size of flat cable connector is to be determined.
- The distance between 2 flat cable connectors are 4mm.

