

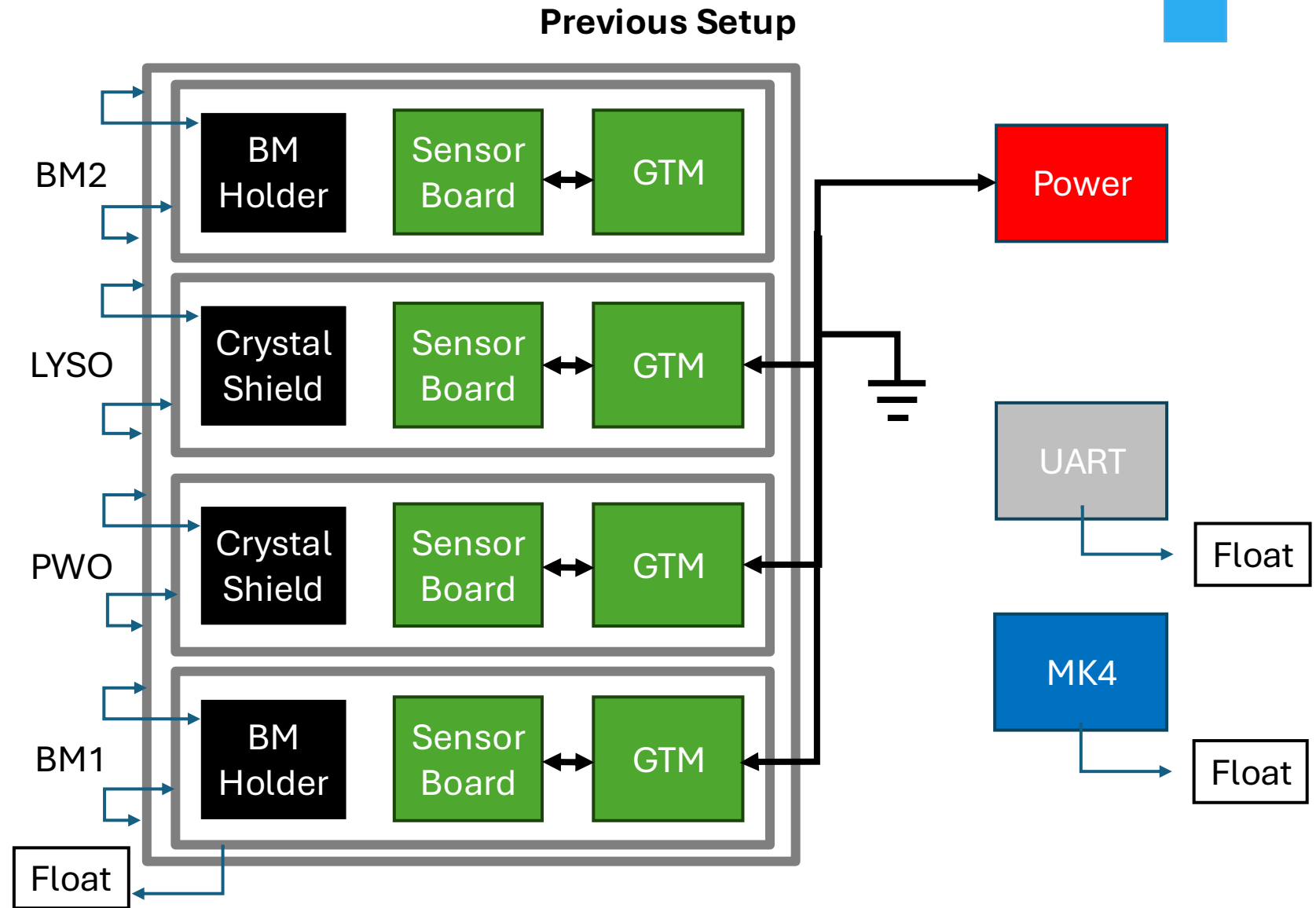


Status Report

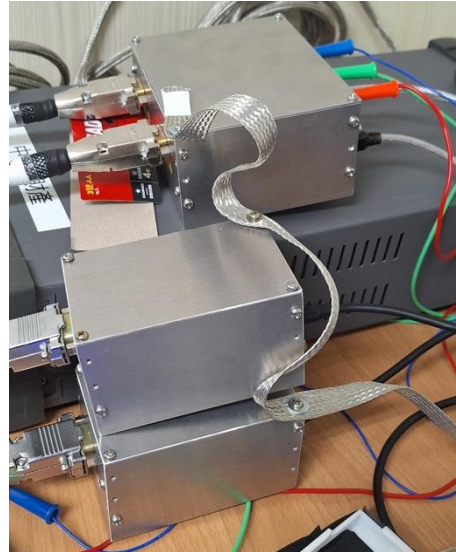
朱珈聖 Chu, Chia-Sheng



Noise Test

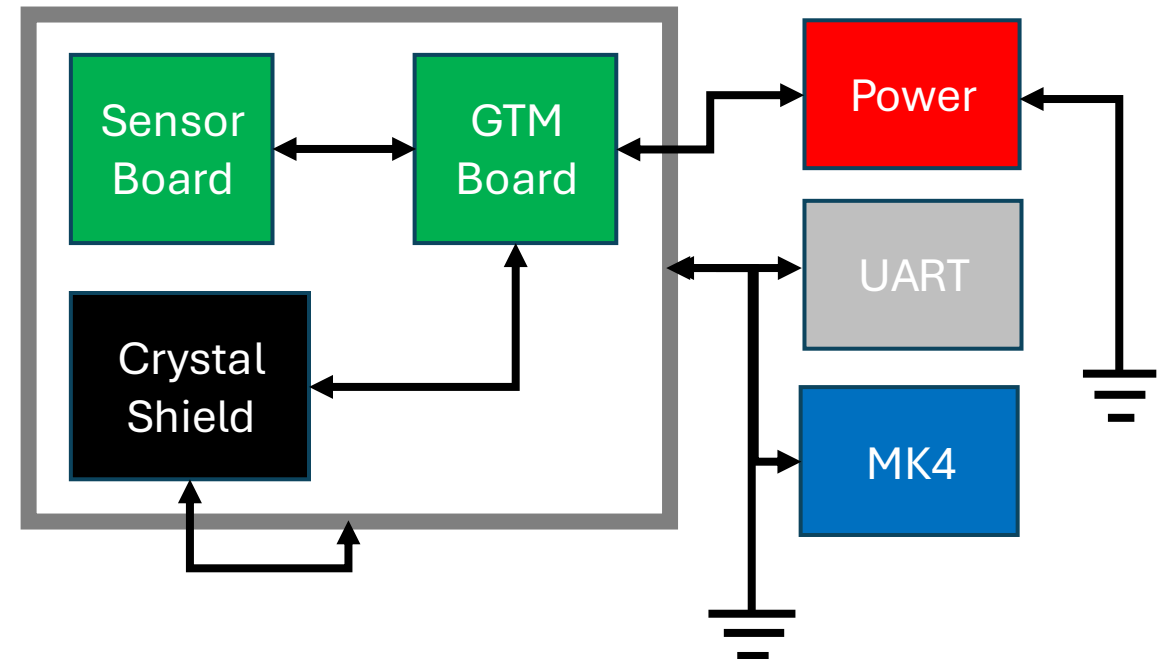


Noise Test



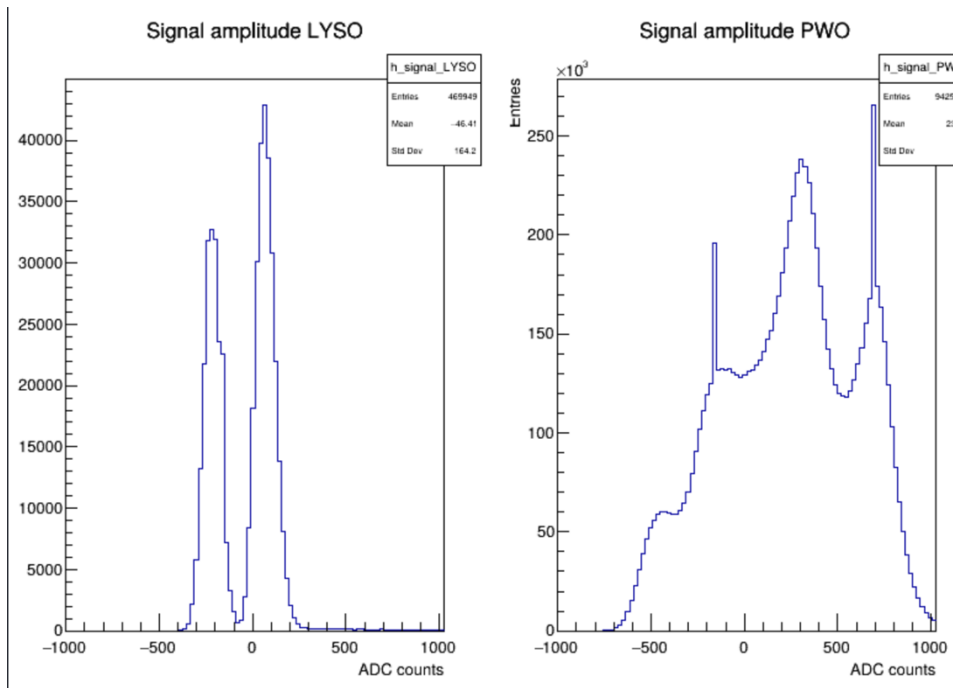
- Use the aluminum foil to cover the Al extr. Frame.
- Use the wire to connect the GTM and crystal shield.
- Use braided sleeving to connect the frame, UART and MK4.

Current Setup

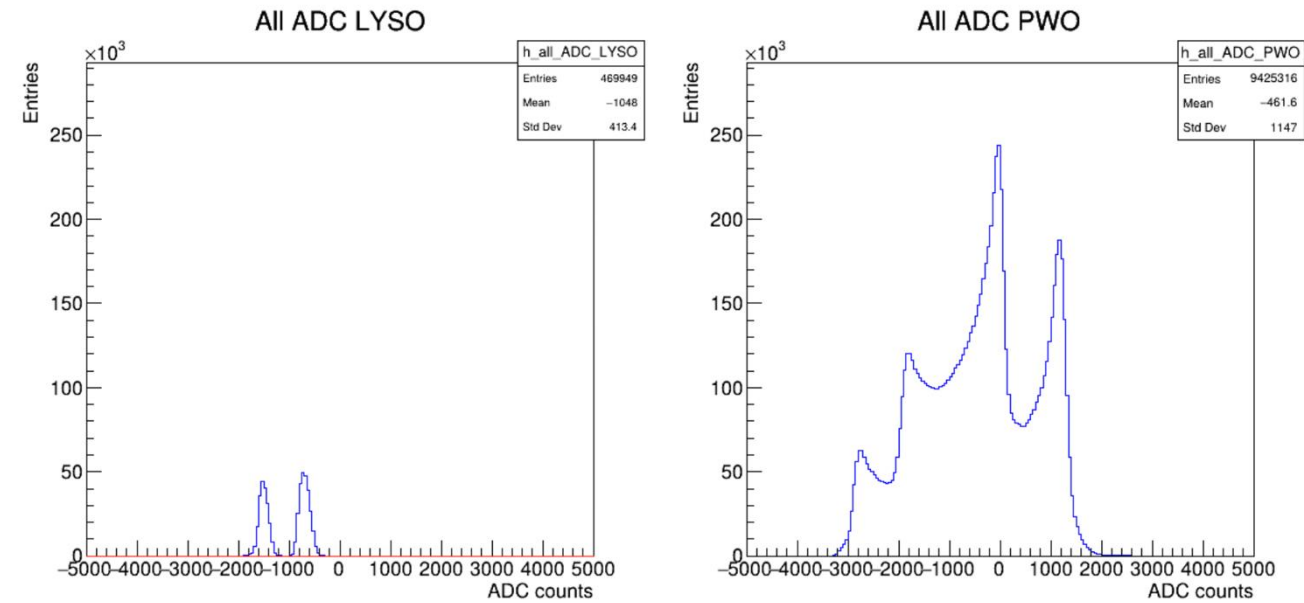


Noise Result

ADCmax



ADC all



Power Outage

No HV:



- Power outage occur in NCU at AM 3:00
- The CitiROC is normal

With HV:



Run4081_HV210_VF

Run4081_HV15_VF

Run4081_HV15_8_VF

Backu

LYSO PPS is strar

Data Path
C:\Users\USER\OneDrive

appended path
C:\Users\HEP509\Desktop\GTMv

Data Path
C:\Users\HEP509\Desktop\GTMv

Select board
V2-001

Baud Ra
115200

Load ROC A cfg (*.txt)
Load

Record
START

Load ROC B cfg (*.txt)
Load

Save cfg (*.t
Save

cfg FPGA
START

Close
STOP

ReWrite HV
ReWrite HV
15

ReWrite Gain
ReWrite Gain
x40

ReWrite FV
ReWrite FV

ROC A Thresh
VF(Hex)
9C

ROC A & B FV
Disconnect

VF(DEC)
230

PPS Selection
Disable

HG/LG Mode
LG Only

Read Buffer Out A
0

Read Buffer
0

Buffer Out A
0

Buffer Out
0

A Receive
0

B Receive
0

Output Mode
All

LYSO & PWO4 IP
192.168.0.101
Beam Monitor IP
192.168.0.102
Stage IP

Filename Include:
Energy HV DAC VF DAC PreAmp Position

X(mm)
0
Y(mm)
0
Z(mm)
0

Save Data Save Mode ReSync Stop Button

Save Single ReSync Stop

Saving 1
Saving 2

Energy Run sec per file Remaining Time(s) Other Comment

<0> 0 180 180

LYSO Control Panel Rotate 0

HV DAC 210 HV DAC Fine 0 On/Off Connecting

ROC A VF(DEC) 280 ROC B VF(DEC) 280 Send cfg

HG PreAmp x600 LG PreAmp x60 Send

LYSO Read Panel LYSO Temp 0 LYSO receive 41238 LYSO Data

HV DAC A 210 HV DAC B 210 HV Fine 0 Latest PPS counter 3769 Latest Fine time 208333

Trigger counter A 0 Trigger counter B 0

Live time A (buf) 100 Live time B (buf) 100

CITIROC A Hit Counter CITIROC B Hit Counter

PbWO4 Control Panel Rotate 0

HV DAC 16 HV DAC Fine 0 On/Off Connecting

ROC A VF(DEC) 280 ROC B VF(DEC) 280 Send cfg

HG PreAmp x40 LG PreAmp x4 Send

PbWO4 Read Panel PbWO4 Temp 0

HV DAC A 16 HV DAC B 16 HV Fine 0 Latest PPS counter 3769 Latest Fine time 208333

Trigger counter A 58 Trigger counter B 2086

Live time A (buf) 98.59 Live time B (buf) 54.29

CITIROC A Hit Counter CITIROC B Hit Counter

Beam Monitor 1 Control Panel

HV DAC 0 HV DAC Fine 0 On/Off Connecting

ROC A VF(DEC) 0 ROC B VF(DEC) 0 Send cfg

HG PreAmp x9.5 LG PreAmp x0.95 Send

Beam Monitor 1 Read Panel Monitor 1 Temp 0 Monitor 1 receive 0 Monitor 1 Data

HV DAC A 0 HV DAC B 0 HV Fine 0 Latest PPS counter 0 Latest Fine time

Trigger counter A 0 Trigger counter B 0

Live time A (buf) 0 Live time B (buf) 0

CITIROC A Hit Counter CITIROC B Hit Counter

Beam Monitor 2 Control Panel

HV DAC 0 HV DAC Fine 0 On/Off Disable

ROC A VF(DEC) 0 ROC B VF(DEC) 0 Send cfg

HG PreAmp x9.5 LG PreAmp x0.95 Send

Beam Monitor 2 Read Panel Monitor 2 Temp 0 Monitor 2 receive 0 Monitor 2 Data

HV DAC A 0 HV DAC B 0 HV Fine 0 Latest PPS counter 0 Latest Fine time

Trigger counter A 0 Trigger counter B 0

Live time A (buf) 0 Live time B (buf) 0

CITIROC A Hit Counter CITIROC B Hit Counter

LYSO path Monitor1 path PbWO4 path LYSO Data Path Monitor2 path Monitor Data Path Stage path Temp path

Error 1445 occurred at Open VI Reference in Remote_Init.vi->Remote_Control_Panel.vi

Possible reason(s):
LabVIEW: Open VI Reference no longer matches VIs in memory by filename. A filename is no longer sufficient because the full name of a VI now includes any owning libraries.
VI Path: C:\Users\USER\Desktop\GTMv3_4\FTD2XX_4P_revF_pei 240723 LV2014\FTD2XX_4P_revF_pei 240723_Monitor1.vi
Search ni.com for error Continue Stop